TRIAC DRIVER

TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRIAC

TLP3527

PROGRAMMABLE CONTROLLERS

AC-OUTPUT MODULE

SOLID STATE RELAY

The TOSHIBA TLP3527 consists of a zero voltage crossing turn-on photo-triac optically coupled to a gallium arsenide infrared emitting diode in a 16 lead plastic DIP package.

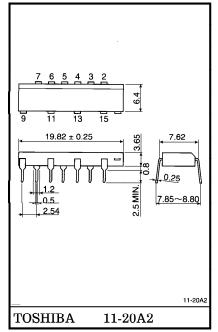
Peak Off-State Voltage : 600V (MIN.)

Trigger LED Current : 10mA (MAX.)

On-State Current : $1.0A_{rms}(MAX.)$

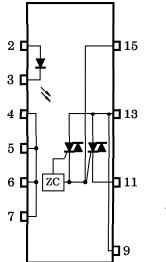
Isolation Voltage $: 2500 V_{rms} (MIN.)$

UL Recognized : UL1577, File No. E67349 Unit in mm



Weight: 1.13g

PIN CONFIGURATION (TOP VIEW)



2: ANODE 3: CATHODE

4, 5, 6, 7 : N.C.

9, 13 : TRIAC T2 11: TRIAC T1 15: TRIAC GATE

- TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

 Gallium arsenide (GaAs) is a substance used in the products described in this document. GaAs dust and fumes are toxic. Do not break, cut or products with other industrial waste or with domestic garbage.

 The products described in this document are subject to foreign exchange and foreign trade control laws.

 The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.

MAXIMUM RATINGS (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	MBOL RATING			
	Forward Current	$I_{\mathbf{F}}$	50	mA		
Оре	Forward Current Derating (Ta≥53	B°C)	⊿I _F /°C	-0.7	mA/°C	
[E]	Peak Forward Current (100 µs puls	se, 100pps)	$I_{ extbf{FP}}$	1	Α	
OPETECTOR LE	Reverse Voltage	$ m v_R$	5	V		
	Junction Temperature		T_{j}	125	°C	
	Off-State Output Terminal Voltage	$v_{ m DRM}$	600	V		
Ж	O. St. L. DMS C.	$Ta = 40^{\circ}C$	Im (Daro)	1.0	A	
$_{ m T0}$	On-State KMS Current	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	IT (RMS)	0.7		
EC	On-State Current Derating (Ta≥4	0°C)	$\Delta I_{\mathrm{T}}/^{\circ}\mathrm{C}$	-14.3	mA/°C	
DET	Peak Current from Snubber Circui $(100\mu s$ pulse, $120pps)$	I_{SP}	2	A		
	Peak Nonrepetitive Surge Current	(50Hz, Peak)	I_{TSM}	10	Α	
	Junction Temperature	T_{j}	110	°C		
Sto	rage Temperature Range	rature Range $T_{\rm stg}$ $-40{\sim}125$				
Оре	erating Temperature Range		${ m T_{opr}}$			
Lea	d Soldering Temperature (10s)	iture (10s)		260	$^{\circ}\mathrm{C}$	
Isol	ation Voltage (AC, 1 min., R.H.≤6	0%) (Note)	$BV_{\mathbf{S}}$	2500	V _{rms}	

(Note) Device considered a two terminal : LED side pins shorted together and DETECTOR side pins shorted together.

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	v_{AC}	_	_	240	Vac
Forward Current	${ m I_F}$	15	20	25	mA
Peak Current from Snubber Circuit	I_{SP}	_	_	1	Α
Operating Temperature	$T_{ m opr}$	-20	_	80	°C

INDIVIDUAL ELECTRICAL CHARACTERISTICS (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
D	Forward Voltage	$V_{\mathbf{F}}$	$I_{\mathbf{F}} = 10 \text{mA}$	1.0	1.15	1.3	V
LE]	Reverse Current	${ m I_R}$	$V_R = 5V$	_	_	10	μ A
	Capacitance	C_{T}	V=0, f=1MHz	_	30	_	pF
	Peak Off-State Current	$I_{ m DRM}$	V _{DRM} =600V, Ta=110°C	_	_	100	μ A
OR	Peak On-State Voltage	$ m V_{TM}$	$I_{TM} = 1.5A$		_	3.0	V
CT(Holding Current	$ m I_{H}$	$R_L = 100\Omega$	_		25	mA
DETE	Critical Rate of Rise of Off-State Voltage	dv / dt	$V_{in} = 240V_{rms}$ (Fig.1)	_	500	_	V/μs
	Critical Rate of Rise of Commutating Voltage	dv / dt (c)	$ m V_{in}$ = 240 $ m V_{rms}$, $ m I_{T}$ = 1.0 $ m A_{rms}$ (Fig.1)	_	5	_	V/μs

COUPLED ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Trigger LED Current	I_{FT}	$V_T = 6V$	_		10	mA
Inhibit Voltage	$ m v_{IH}$	I _F =Rated I _F T	_		50	V
Leakage in Inhibited State	$I_{ m IH}$	I _F =Rated I _{FT} V _T =Rated V _{DRM}		200	_	μ A
Capacitance (Input to Output)	c_{S}	$V_S=0$, f=1MHz	_	1.5	_	pF
Isolation Resistance	$R_{\mathbf{S}}$	$V_S = 500V$	5×10^{10}	10^{14}	_	Ω
	BVS	AC, 1 minute	2500	_	_	77
Isolation Voltage		AC, 1 second, in oil	_	5000	_	$ V_{ m rms} $
		DC, 1 minute, in oil	_	5000	_	v_{dc}

Fig.1: dv/dt TEST CIRCUIT

