



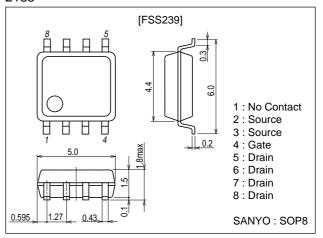
# **Load Switching Applications**

#### **Features**

- · Low ON resistance.
- 2.5V drive.

#### **Package Dimensions**

unit : mm 2185



## **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		7	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	52	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1000mm <sup>2</sup> X 0.8mm)	1.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	O IIII
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	20			٧
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =20V, V <sub>GS</sub> =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	$V_{GS}=\pm 8V$ , $V_{DS}=0$			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	0.4		1.3	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =7A	10.9	15.5		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =7A, V <sub>GS</sub> =4V		24	32	mΩ
	RDS(on)2	ID=2A, VGS=2.5V		29	42	mΩ

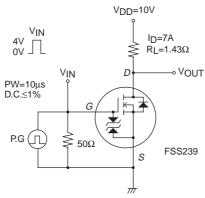
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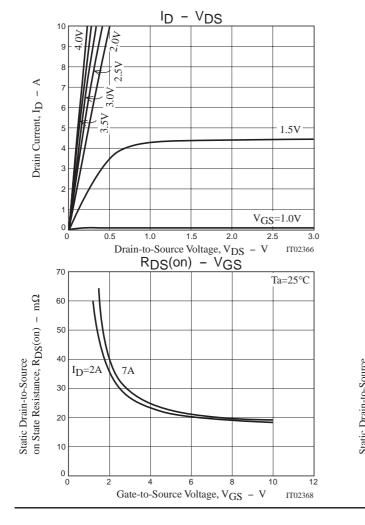
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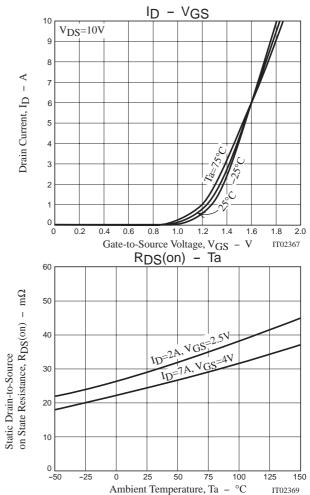
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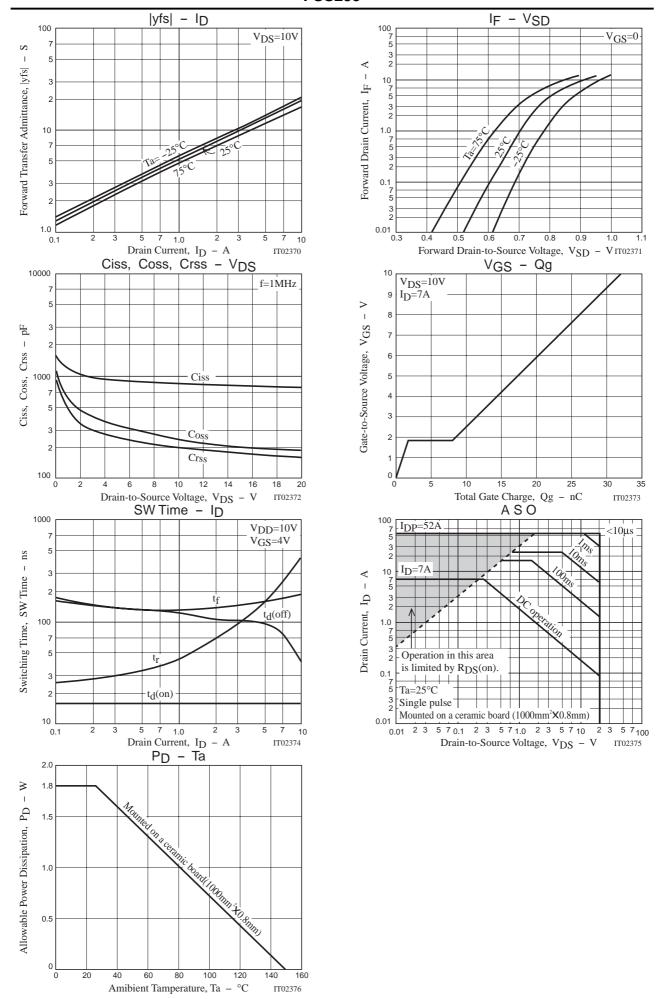
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V <sub>DS</sub> =10V, f=1MHz		900		pF
Output Capacitance	Coss	V <sub>DS</sub> =10V, f=1MHz		260		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =10V, f=1MHz		200		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		15		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		220		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		75		ns
Fall Time	tf	See specified Test Circuit		180		ns
Total Gate Charge	Qg	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =7A		32		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =7A		1.7		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =10V, V <sub>GS</sub> =10V, I <sub>D</sub> =7A		6.4		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =7A, V <sub>GS</sub> =0		0.86	1.2	V

## **Switching Time Test Circuit**









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