

DATA SHEET

TDA1523

Stereo cassette head
preamplifier and equalizer

Preliminary specification
File under Integrated Circuits, IC01

1996 Feb 26

Stereo cassette head preamplifier and equalizer

TDA1523

FEATURES

- Two independent amplifiers with open-loop gain of 90 dB (typical)
- Internal DC feedback via 140 k Ω resistor from output to feedback point
- AC characteristics that can be determined externally by an RC network
- Electronic on/off switching with transient suppression for switch on
- Head input at DC ground that eliminates the input coupling capacitor
- Minimum external component requirement
- Stability down to a gain of 30 dB
- Low input noise
- Low distortion
- DC input current <2 μ A.

GENERAL DESCRIPTION

The TDA1523 is a playback amplifier for car radio/cassette players.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _P	supply voltage (pin 7)		7.5	–	12	V
I _P	supply current (pin 7)		–	5	–	mA
T _{amb}	operating ambient temperature		–30	–	+85	°C
THD	total harmonic distortion		–	0.05	–	%
α_{CS}	channel separation	R _S = 10 k Ω ; L _S = 0	45	–	–	dB

ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
TDA1523	DIP14	plastic dual in-line package; 14 leads (300 mil)	SOT27-1

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BLOCK DIAGRAM

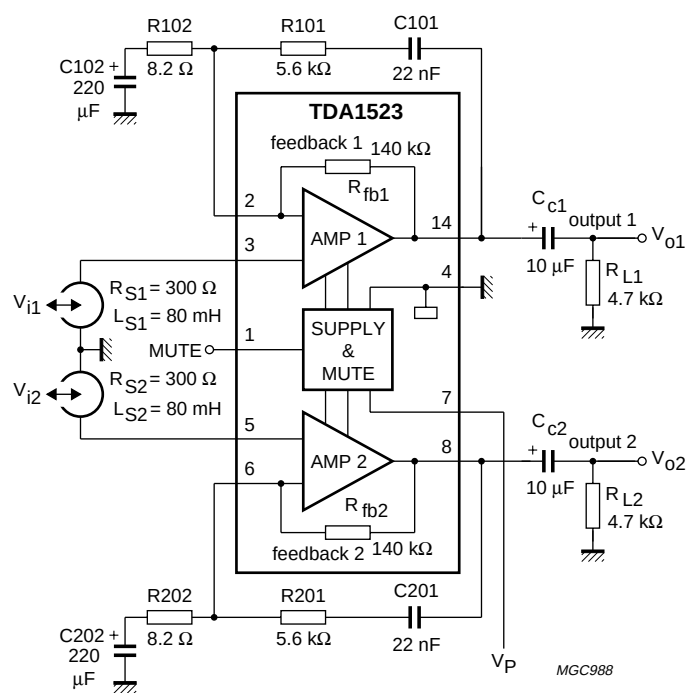


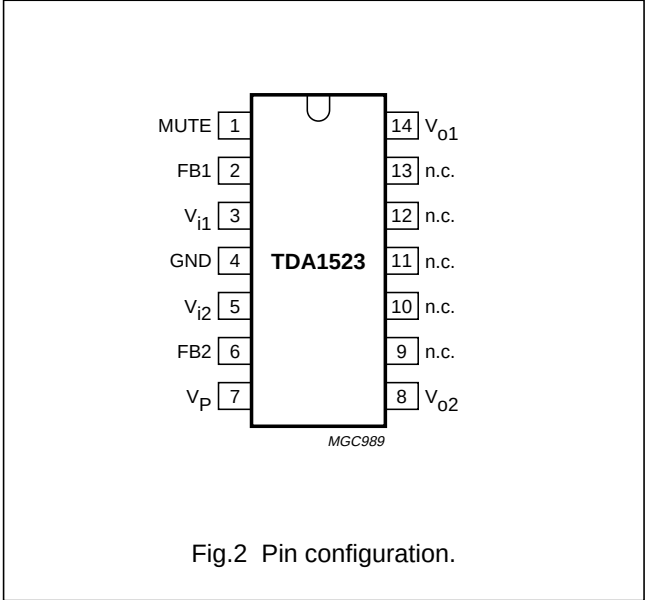
Fig.1 Block diagram with external components; also used as test circuit.

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PINNING

SYMBOL	PIN	DESCRIPTION
MUTE	1	mute input
FB1	2	feedback 1
V _{i1}	3	input voltage 1
GND	4	ground
V _{i2}	5	input voltage 2
FB2	6	feedback 2
V _P	7	power supply
V _{o2}	8	output voltage 2
n.c.	9	not connected
n.c.	10	not connected
n.c.	11	not connected
n.c.	12	not connected
n.c.	13	not connected
V _{o1}	14	output voltage 1



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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134); note 1.

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V_P	supply voltage (pin 7)	7.5	12	V
I_{fb}	feedback current (pins 2 and 6)	–	10	mA
P_{tot}	total power dissipation	–	300	mW
T_{amb}	operating ambient temperature	–30	+85	°C
T_{stg}	storage temperature	–55	+150	°C

Note

1. All pins except 2 and 6 (feedback) can be connected to V_P (pin 7) or ground (pin 4).

CHARACTERISTICS

$V_P = 8.5$ V; $T_{amb} = 25$ °C; see test circuit Fig.1; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Supply (pin 7)						
V_P	supply voltage		7.5	–	12	V
I_P	supply current		–	5	–	mA
Inputs (pins 3 or 5)						
$V_{ni(rms)}$	unweighted noise input voltage (RMS value)	$f = 20$ Hz to 20 kHz; measured in Fig.3	–	1.6	–	μ V
V_{ni}	noise input voltage	$R_S = 0$; $f = 1$ kHz; measured in Fig.3; see also Fig.5	–	5	–	$\frac{nV}{\sqrt{Hz}}$
I_{ni}	noise input current	$f = 1$ kHz; measured in Fig.3; see also Fig.6	–	1.2	–	$\frac{pA}{\sqrt{Hz}}$
I_3, I_5	DC input current at pins 3 and 5		–	–	–2	μ A
$ Z_i $	input impedance	$f = 1$ kHz; note 1	200	–	–	k Ω
Outputs (pins 14 or 8); see Fig.7						
V_o	output voltage	$V_i = 0.3$ mV; $f = 315$ Hz	–	0.72	–	V
		THD = 1%; $f = 1$ kHz	1.0	–	–	V
I_o	output source current	$V_{1-4} \geq 7.5$ V; mute off	–5	–10	–	mA
V_O	DC output voltage		–	3.7	–	V
$V_{no(rms)}$	weighted noise output voltage; DIN A (RMS value)	$R_S = 300 \Omega$; $L_S = 80$ mH	–	700	–	μ V
V_{no}	weighted noise output voltage CCITT (peak value)	$R_S = 300 \Omega$; $L_S = 80$ mH	–	1200	–	μ V
	CCIR (peak value)	$R_S = 300 \Omega$; $L_S = 80$ mH	–	1600	–	μ V
V_{no}	unweighted noise output voltage; DIN 45405 (peak value)	$R_S = 300 \Omega$; $L_S = 80$ mH	–	1800	–	μ V
$ Z_o $	output impedance	$f = 1$ kHz; note 1	–	–	1	k Ω

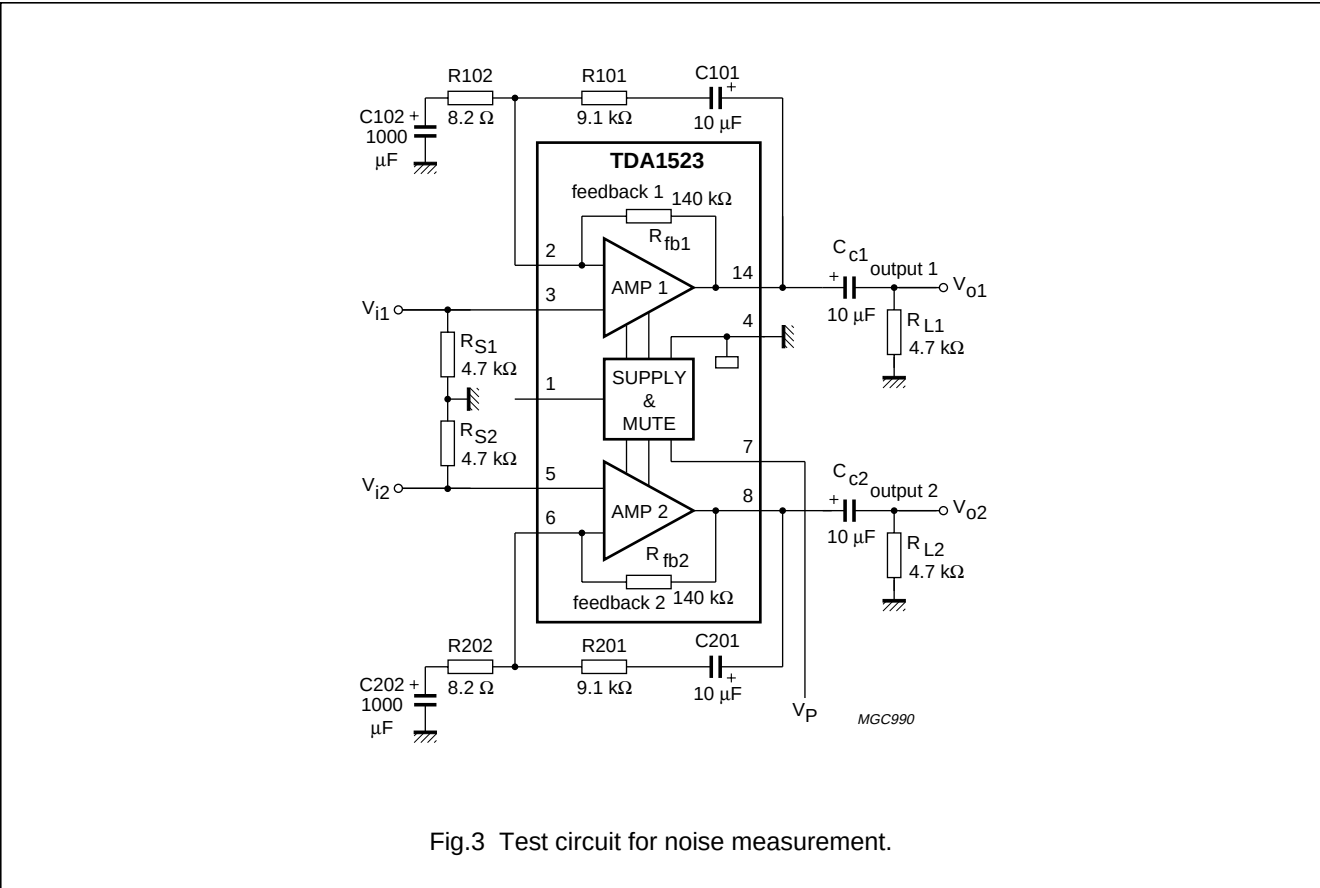
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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Mute on/off (pin 1); see Fig.4						
$V_{\text{mute(on)}}$	mute on voltage	mute switch closed	0	–	1	V
$I_{\text{mute(on)}}$	mute on current	mute switch closed or $V_{1-4} = 0\text{ V}$	–	2.7	–	μA
$V_{\text{mute(off)}}$	mute off voltage	mute switch open	7.5	–	V_P	V
General						
R_{fb}	internal feedback resistor	note 1	100	140	180	$\text{k}\Omega$
G_v	open-loop voltage gain	$f = 315\text{ Hz}$; note 1	–	90	–	dB
α_{CS}	channel separation	$R_S = 10\text{ k}\Omega$; $L_S = 0$; note 2	45	–	–	dB
PSRR	power supply ripple rejection	$V_{P(\text{rms})} = 0.1\text{ V}$; $f = 100\text{ Hz}$; note 3	90	95	–	dB
THD	total harmonic distortion	$f = 1\text{ kHz}$; $V_O = 0.3\text{ V}$; note 4	–	0.05	–	%

Notes

- 1. Applies to each amplifier.
- 2. Frequency range 300 Hz to 20 kHz.
- 3. Referred to the input.
- 4. Measured selective.



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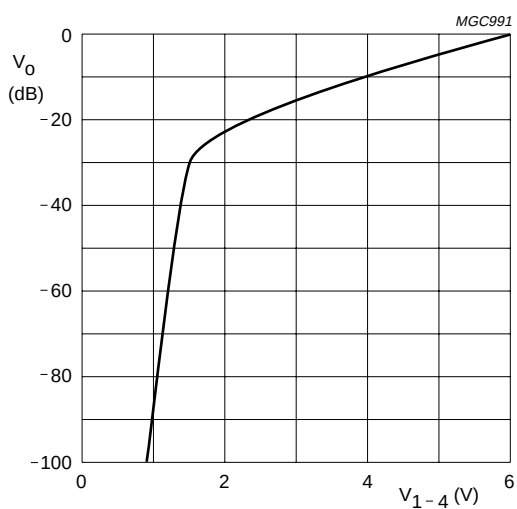
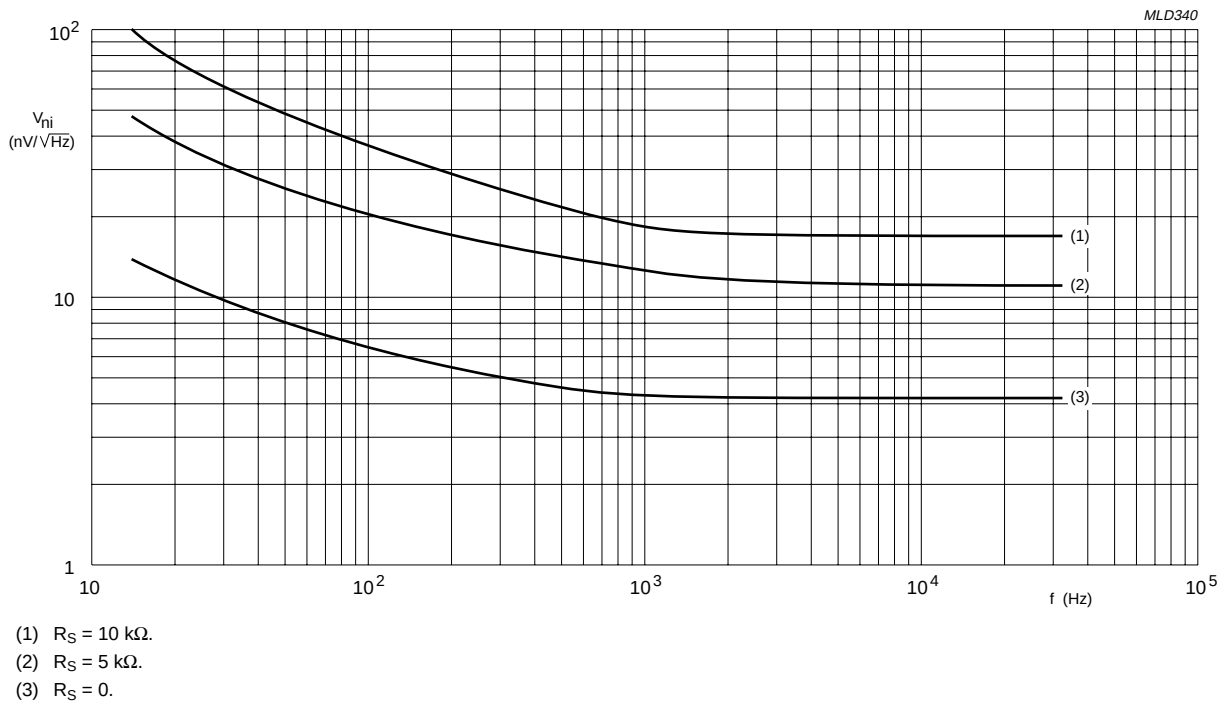


Fig.4 Muting depth as a function of control voltage at pin 1.

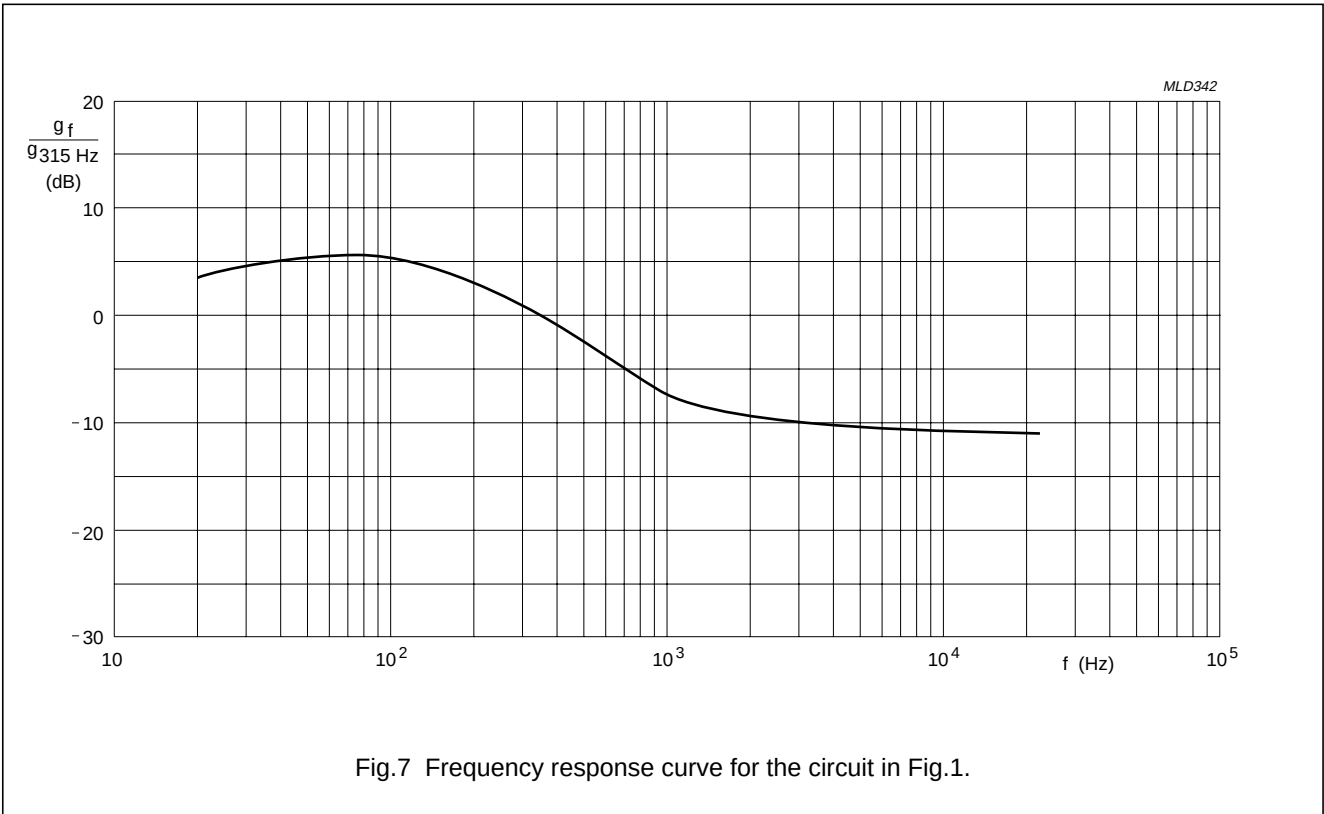
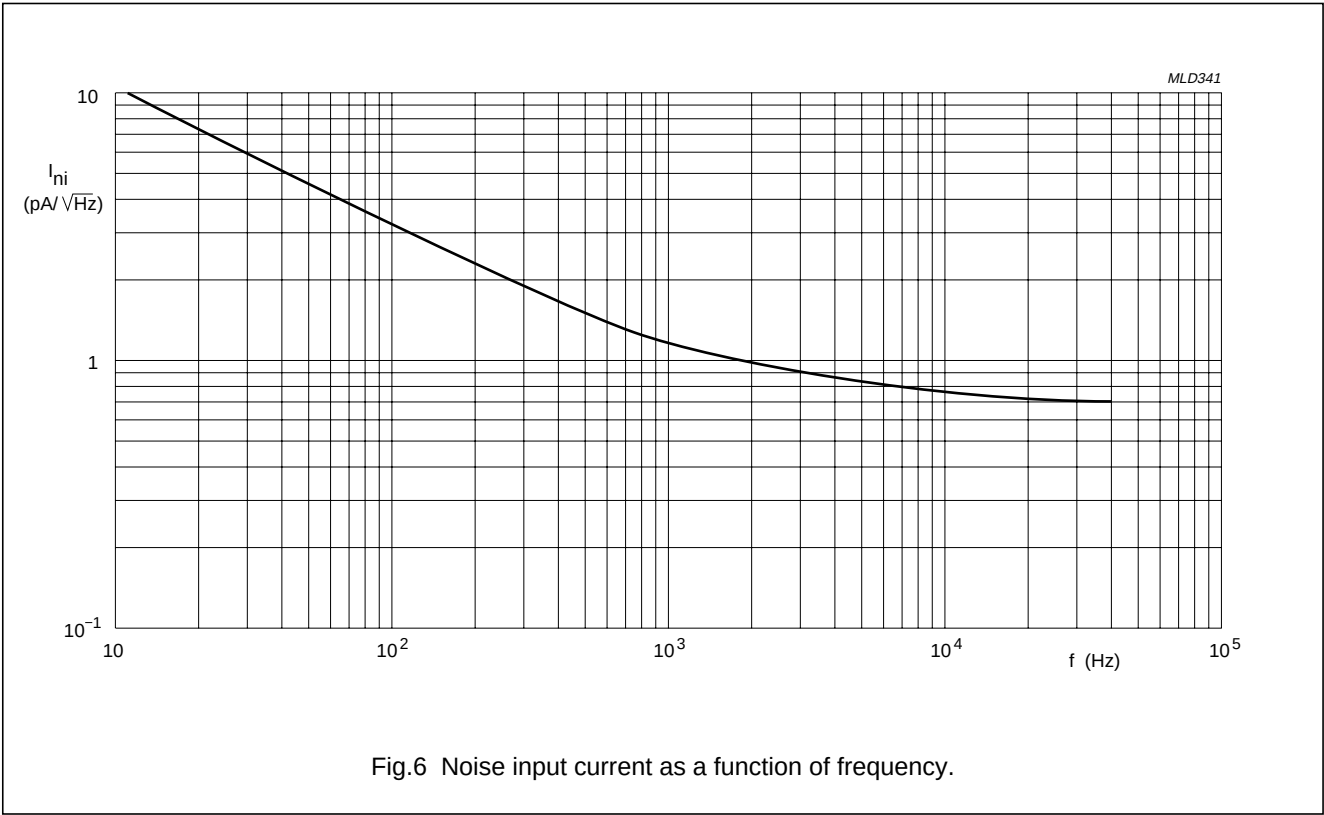


- (1) $R_S = 10 \text{ k}\Omega$.
- (2) $R_S = 5 \text{ k}\Omega$.
- (3) $R_S = 0$.

Fig.5 Noise input voltage as a function of frequency.

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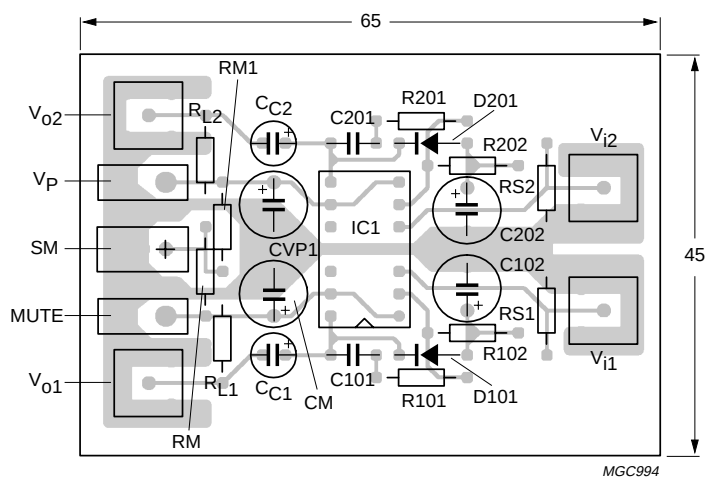
TDA1523**PRINTED-CIRCUIT BOARD**

Fig.8 Printed-circuit board component side, showing component layout of Fig.1 and/or Fig.10.

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APPLICATION INFORMATION

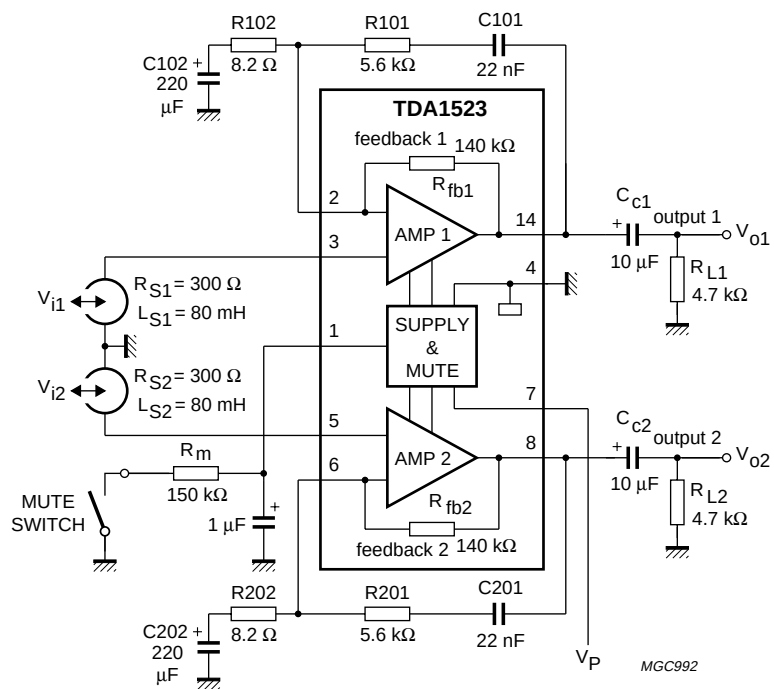


Fig.9 Simple mute application.

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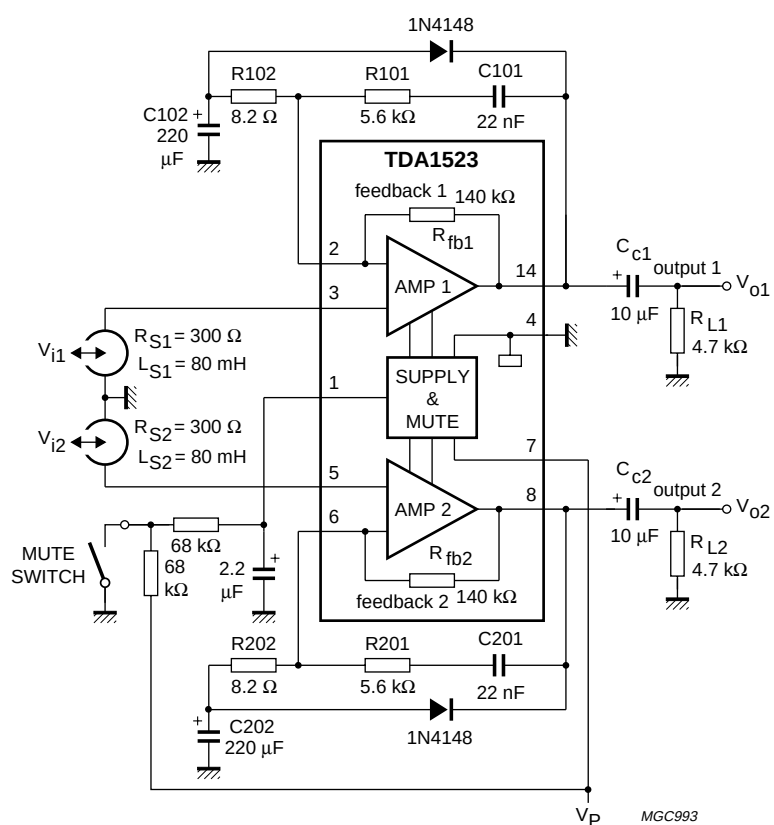


Fig.10 Application for plop-free muting.

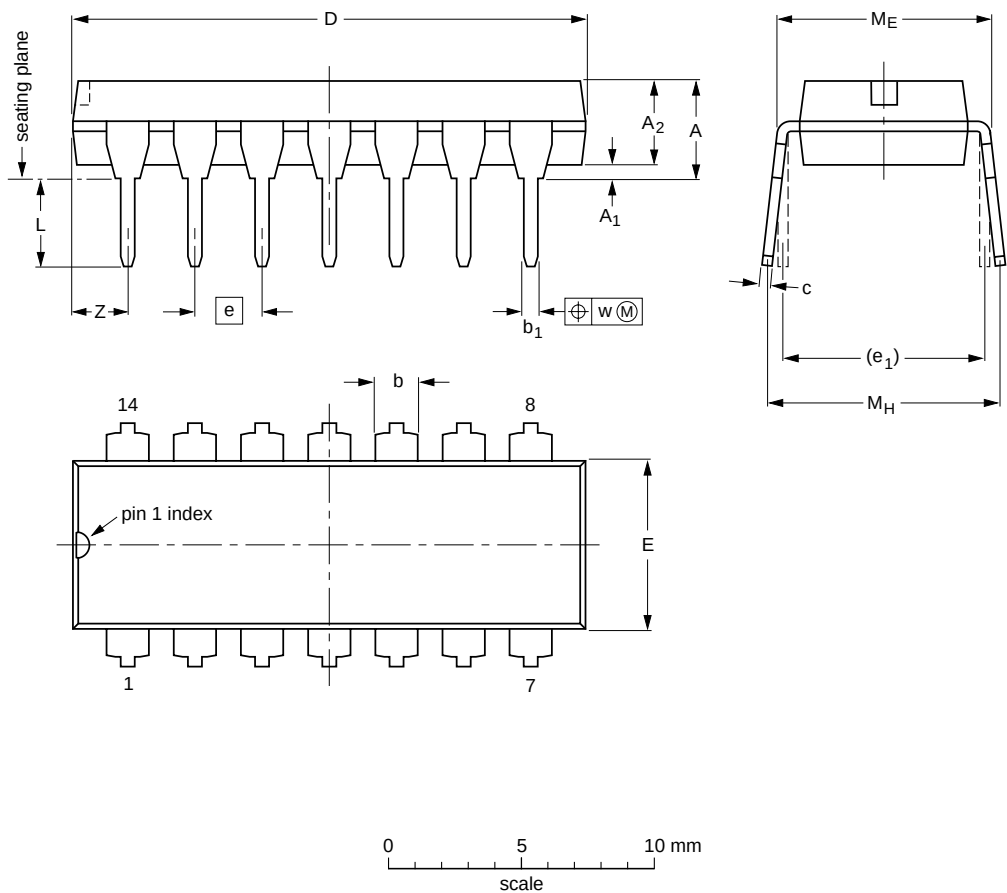
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PACKAGE OUTLINE

DIP14: plastic dual in-line package; 14 leads (300 mil)

SOT27-1

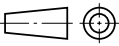


DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	c	D ⁽¹⁾	E ⁽¹⁾	e	e ₁	L	M _E	M _H	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.13	0.53 0.38	0.36 0.23	19.50 18.55	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	2.2
inches	0.17	0.020	0.13	0.068 0.044	0.021 0.015	0.014 0.009	0.77 0.73	0.26 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.087

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT27-1	050G04	MO-001AA				92-11-17 95-03-11

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SOLDERING

Introduction

There is no soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and surface mounted components are mixed on one printed-circuit board. However, wave soldering is not always suitable for surface mounted ICs, or for printed-circuits with high population densities. In these situations reflow soldering is often used.

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our *"IC Package Databook"* (order code 9398 652 90011).

Soldering by dipping or by wave

The maximum permissible temperature of the solder is 260 °C; solder at this temperature must not be in contact with the joint for more than 5 seconds. The total contact time of successive solder waves must not exceed 5 seconds.

The device may be mounted up to the seating plane, but the temperature of the plastic body must not exceed the specified maximum storage temperature ($T_{stg\ max}$). If the printed-circuit board has been pre-heated, forced cooling may be necessary immediately after soldering to keep the temperature within the permissible limit.

Repairing soldered joints

Apply a low voltage soldering iron (less than 24 V) to the lead(s) of the package, below the seating plane or not more than 2 mm above it. If the temperature of the soldering iron bit is less than 300 °C it may remain in contact for up to 10 seconds. If the bit temperature is between 300 and 400 °C, contact may be up to 5 seconds.

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

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NOTES

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Philips Semiconductors – a worldwide company

Argentina: IEROD, Av. Juramento 1992 - 14.b, (1428)
BUENOS AIRES, Tel. (541)786 7633, Fax. (541)786 9367

Australia: 34 Waterloo Road, NORTH RYDE, NSW 2113,
Tel. (02)805 4455, Fax. (02)805 4466

Austria: Triester Str. 64, A-1101 WIEN, P.O. Box 213,
Tel. (01)60 101-1236, Fax. (01)60 101-1211

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Brazil: Rua do Rocio 220 - 5th floor, Suite 51,
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Chile: Av. Santa Maria 0760, SANTIAGO,
Tel. (02)773 816, Fax. (02)777 6730

China/Hong Kong: 501 Hong Kong Industrial Technology Centre,
72 Tat Chee Avenue, Kowloon Tong, HONG KONG,
Tel. (852)2319 7888, Fax. (852)2319 7700

Colombia: IPRELENZO LTDA, Carrera 21 No. 56-17,
77621 BOGOTA, Tel. (571)249 7624/(571)217 4609,
Fax. (571)217 4549

Denmark: Prags Boulevard 80, PB 1919, DK-2300
COPENHAGEN S, Tel. (45)32 88 26 36, Fax. (45)31 57 19 49

Finland: Sinikalliontie 3, FIN-02630 ESPOO,
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France: 4 Rue du Port-aux-Vins, BP317,
92156 SURESNES Cedex,
Tel. (01)4099 6161, Fax. (01)4099 6427

Germany: P.O. Box 10 51 40, 20035 HAMBURG,
Tel. (040)23 53 60, Fax. (040)23 53 63 00

Greece: No. 15, 25th March Street, GR 17778 TAVROS,
Tel. (01)4894 339/4894 911, Fax. (01)4814 240

India: Philips INDIA Ltd, Shivsagar Estate, A Block,
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Tel. (022)4938 541, Fax. (022)4938 722

Indonesia: Philips House, Jalan H.R. Rasuna Said Kav. 3-4,
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Tel. (021)5201 122, Fax. (021)5205 189

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Italy: PHILIPS SEMICONDUCTORS S.r.l.,
Piazza IV Novembre 3, 20124 MILANO,
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Japan: Philips Bldg 13-37, Kohnan 2-chome, Minato-ku, TOKYO 108,
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Tel. (03)301 6312, Fax. (03)301 42 43

Sweden: Kottbygatan 7, Akalla. S-164 85 STOCKHOLM,
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Road, Sec. 1. Taipei, Taiwan ROC, P.O. Box 22978,
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Ukraine: Philips UKRAINE, 2A Akademika Koroleva str., Office 165,
252148 KIEV, Tel. 380-44-4760297, Fax. 380-44-4766991

United Kingdom: Philips Semiconductors LTD.,
276 Bath Road, Hayes, MIDDLESEX UB3 5BX,
Tel. (0181)730-5000, Fax. (0181)754-8421

United States: 811 East Arques Avenue, SUNNYVALE,
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Telex 35000 phtcnl, Fax. +31-40-2724825

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