

The original source of the μ PC55 is 709, the first general purpose monolithic operational amplifier. It features low offset voltage drift, high output swing under load and low power consumption. The device displays excellent temperature stability and will operate over a wide range of supply voltages.

- μ A709 Direct Replacement

[illegible]

μ PC55A

8 pin Metal Can Package

μ PC55D

8 pin Ceramic DIP
(Dual In-Line Package)

The μPC55A is an 8-pin DIP package. The pin configuration is as follows:

- Pin 1: C_1
- Pin 2: I_1
- Pin 3: I_N
- Pin 4: V^- (CASE)
- Pin 5: C_o
- Pin 6: OUT
- Pin 7: V^+
- Pin 8: C_2

The μPC55D is an 8-pin DIP package. The pin configuration is as follows:

- Pin 1: C_1
- Pin 2: -INPUT
- Pin 3: +INPUT
- Pin 4: V^-
- Pin 5: C_o
- Pin 6: OUT
- Pin 7: V^+
- Pin 8: C_2

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

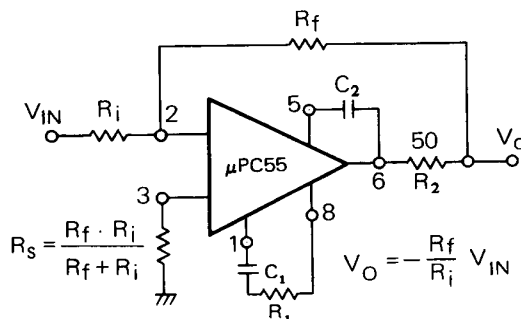
PARAMETER		μPC55	UNIT
Voltage between V ⁺ and V ⁻		36	V
Differential Input Voltage		±6	V
Input Voltage		±10	V
Power Dissipation	A or D Package	300 Note 1	mW
Output Short Circuit Duration		5	s
Operating Temperature Range	A or D Package	-20 to +80	°C
Storage Temperature Range	A Package	-65 to +175	°C
	D Package	-55 to +150	

Note 1 Full Temperature Range

ELECTRICAL CHARACTERISTICS (Ta = 25°C V[±] = ±9V to ±18V)

CHARACTERISTIC	MIN.	TYP.	MAX.	UNIT	CONDITIONS
Input Offset Voltage		0.5	5	mV	R _s ≤ 10 kΩ
Average Input Offset Voltage Drift		2	20	μV/°C	R _s ≤ 50 Ω
Supply Voltage Rejection Ratio		25	100	μV/V	V [±] = ±15V, R _s ≤ 10 kΩ
Input Bias Current		180	800	nA	
Input Offset Current		20	200	nA	
Average Input Offset Current Drift		0.1		nA/°C	
Input Impedance	100	400		kΩ	
Common Mode Input Resistance		10 ⁸		Ω	
Output Resistance		150		Ω	
Power Consumption		80	150	mW	V [±] = ±15 V
Large Signal Voltage Gain	20000	42000		—	V [±] = ±15 V, V _O = ±10V
Output Voltage Swing	±12	±14		V	V [±] = ±15V, R _L ≥ 10 kΩ
	±10	±13		V	V [±] = ±15V, R _L ≥ 2 kΩ
Common Mode Input Voltage Range	±9	±10.3		V	V [±] = ±15V
Common Mode Rejection Ratio	70	90		dB	

Standard Connection Method

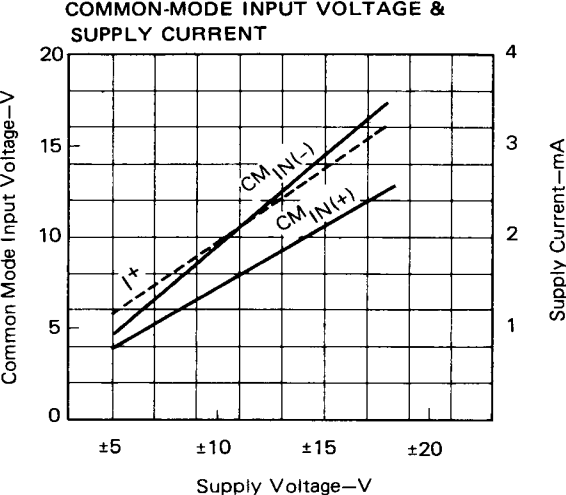
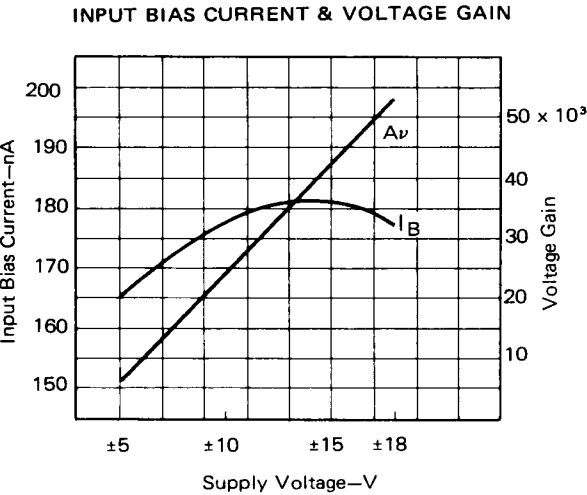
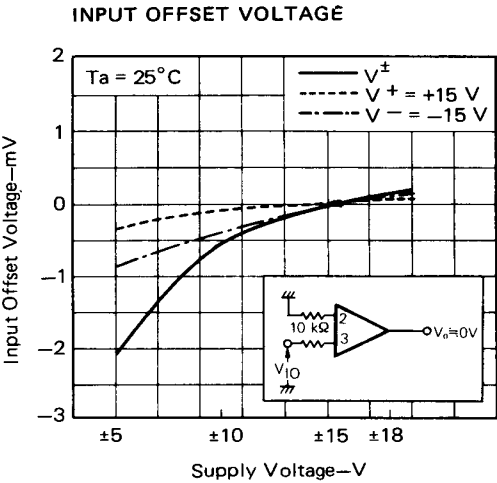
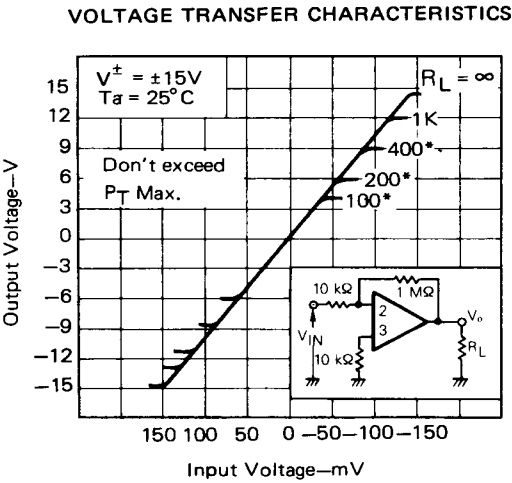
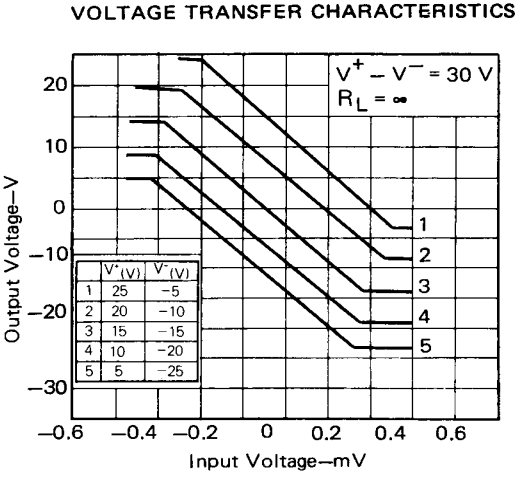
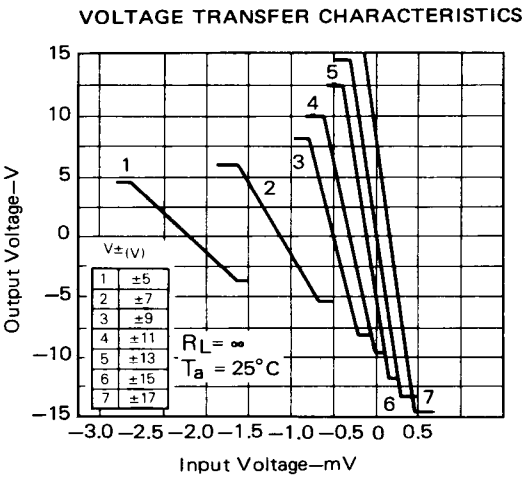


R₂ is necessary when the amplifier is operated with capacitive loading.

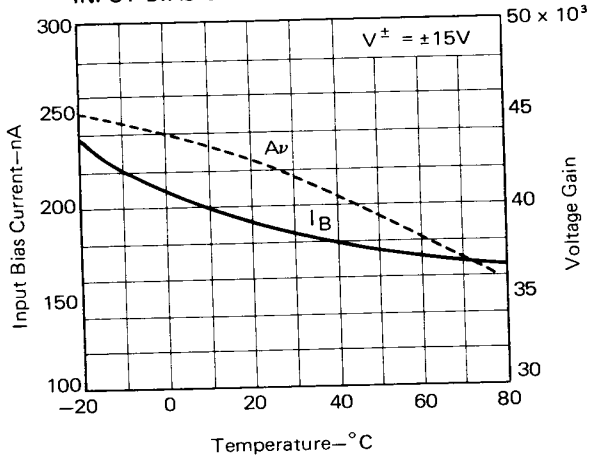
	R _f /R _i	C ₁ (pF)	C ₂ (pF)	R ₁ (kΩ)
A	1000	10	3	0
B	100	100	3	1.5
C	10	500	20	1.5
D	1	5000	200	1.5

Each frequency characteristics is shown in Page 27.

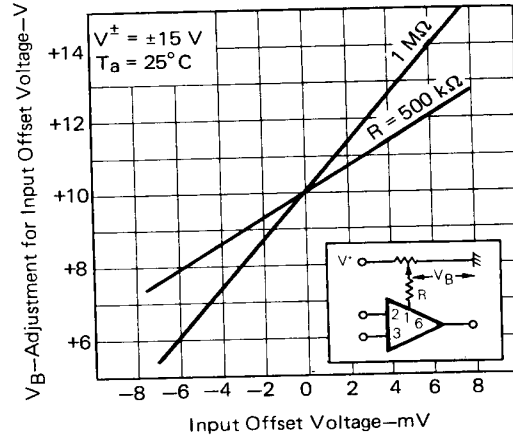
TYPICAL PERFORMANCE CHARACTERISTICS (Ta=25°C)



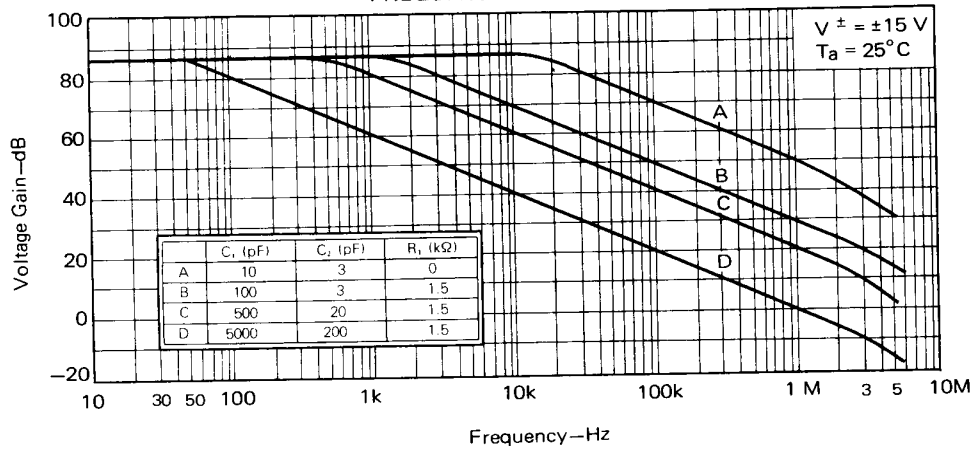
INPUT BIAS CURRENT & VOLTAGE GAIN



ADJUSTMENT FOR INPUT OFFSET VOLTAGE



FREQUENCY RESPONSE



LARGE SIGNAL FREQUENCY RESPONSE

