

**STK4231V** 

# AF Power Amplifier (Split Power Supply) (100W+100W min, THD = 0.08%)

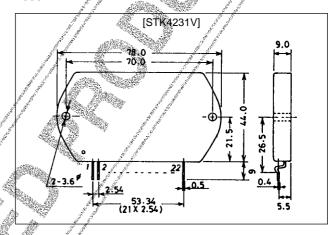
#### **Features**

- Muting circuit built-in to isolate all types of shock noise
- Current mirror circuit for low 0.08% total harmonic distortion
- Pin compatible with the STK4201II series (THD = 0.4%) and the STK4141X series (THD = 0.02%)

## Package Dimensions

unit: mm

#### 4086A



# **Specifications**

## **Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CG</sub> max		±75	V
Thermal resistance	θj-c		1.2	°C/W
Junction temperature	AssaraTi Sas	get and get a second an	150	°C
Operating substrate temperature	To ,	11	125	°C
Storage temperature	Tstg		-30 to +125	°C
Available time for load short-circuit	t <sub>s</sub>	$V_{CC} = \pm 51V, R_L = 8\Omega,$ $f = 50Hz, P_O = 100W$	1	S

## **Recommended Operating Conditions** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>		±51	V
Load resistance	$R_{L}$		8	Ω

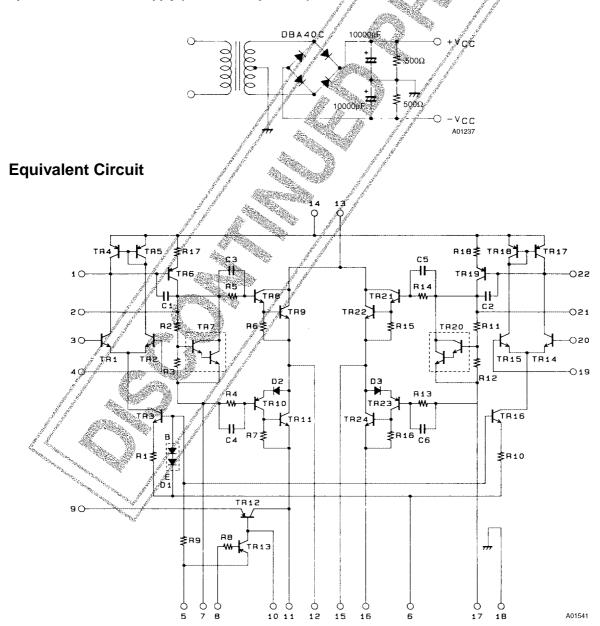
# $\textbf{Operating Characteristics} \ \ \mathrm{at} \ Ta = 25^{\circ}C, \ V_{CC} = \pm 51V, \ R_{L} = 8\Omega \ (noninductive \ load), \ Rg = 600\Omega, \ VG = 40dB$

Parameter	Symbol	Conditions	min	typ	max	Unit
Quiescent current	I <sub>cco</sub>	V <sub>CC</sub> = ±61.5V	20	40 🖟	1,00	mA
Output power	P <sub>O</sub>	THD = 0.08%, f = 20Hz to 20kHz	100	1/	<u></u>	W
Total harmonic distortion	THD	$P_{O} = 1.0W, f = 1kHz$	-	/ s -	0.08	98
Frequency response	f <sub>L</sub> , f <sub>H</sub>	$P_{O} = 1.0W, {}^{+0}_{-3}dB$	-	20 to 50k	e Top	,/ <sup>t</sup> H <b>z</b>
Input impedance	r <sub>i</sub>	P <sub>O</sub> = 1.0W, f = 1kHz	- grad	<i>s</i> 55	<b>%</b> -	/kΩ
Output noise voltage <sup>2</sup>	V <sub>NO</sub>	$V_{CC} = \pm 61.5 \text{V}, \text{Rg} = 10 \text{k}\Omega$	- 100	- N	1.2	mVrms
Neutral voltage	V <sub>N</sub>	V <sub>CC</sub> = ±61.5V	<del>_</del> 70	0'	+70 - 470	mV
Muting voltage	V <sub>M</sub>		/ <sup>*</sup> / <sup>2</sup>	%/>. <b>≔5</b>	<del>_</del> 10,	V

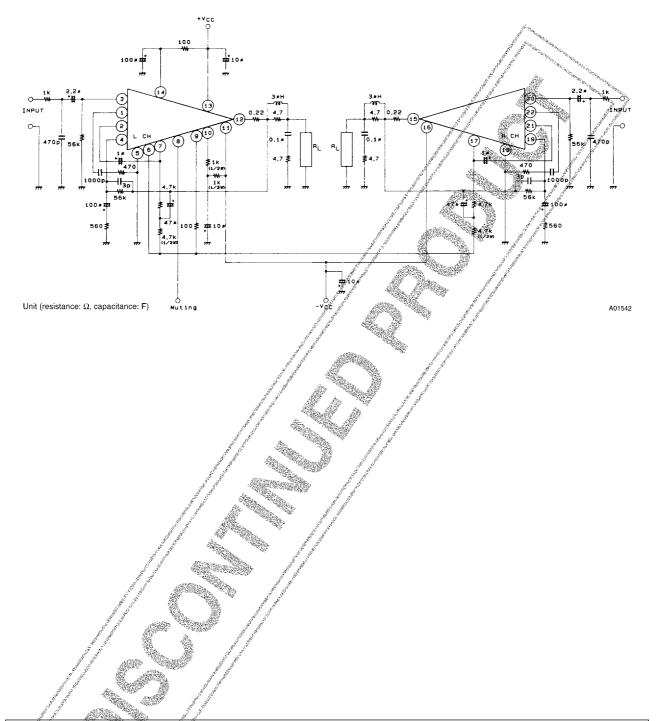
- All tests are measured using a regulated voltage supply unless otherwise specified.
- 1. Available time for load short-circuit and output noise voltage are measured using the transformer supply specified below.

  2. The output noise voltage is the peak value of an average-reading meter with an rms value scale (VTVM). The noise voltage waveform includes no flicker noise.

#### Specified Transformer Supply (MG-200 or Equivalent)



## Sample Application Circuit (100W min 2-Channel AF Power Amplifier)



- No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.
- Anyone purchasing any products described or contained herein for an above-mentioned use shall:
  - Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use:
  - Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees, jointly or severally.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of June, 1997. Specifications and information herein are subject to change without notice.