

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE ( $\pi$ -MOSV)

# 2SK2417

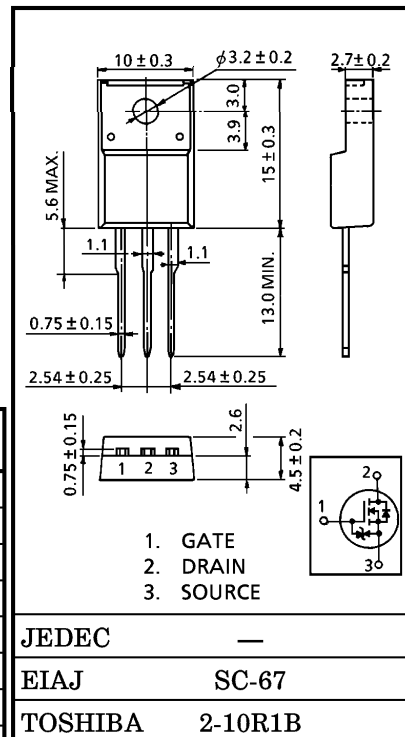
HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS  
 CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

INDUSTRIAL APPLICATIONS  
 Unit in mm

- Low Drain-Source ON Resistance :  $R_{DS(ON)} = 0.42\Omega$  (Typ.)
- High Forward Transfer Admittance :  $|Y_{fs}| = 7.5S$  (Typ.)
- Low Leakage Current  
 :  $I_{DSS} = 100\mu A$  (Max.) ( $V_{DS} = 250V$ )
- Enhancement-Mode  
 :  $V_{th} = 1.5 \sim 3.5V$  ( $V_{DS} = 10V, I_D = 1mA$ )

MAXIMUM RATINGS ( $T_a = 25^\circ C$ )

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	$V_{DSS}$	250	V
Drain-Gate Voltage ( $R_{GS} = 20k\Omega$ )	$V_{DGR}$	250	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current	DC	$I_D$	7.5
	Pulse	$I_{DP}$	30
Drain Power Dissipation ( $T_c = 25^\circ C$ )	$P_D$	30	W
Single Pulse Avalanche Energy**	$E_{AS}$	110	mJ
Avalanche Current	$I_{AR}$	7.5	A
Repetitive Avalanche Energy*	$E_{AR}$	3	mJ
Channel Temperature	$T_{ch}$	150	$^\circ C$
Storage Temperature Range	$T_{stg}$	$-55 \sim 150$	$^\circ C$



Weight : 1.9g

OTHER CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th(ch-c)}$	4.16	$^\circ C/W$
Thermal Resistance, Channel to Ambient	$R_{th(ch-a)}$	62.5	$^\circ C/W$

Note ;

- \* Repetitive rating ; Pulse Width Limited by Max. junction temperature.
- \*\*  $V_{DD} = 50V$ , Starting  $T_{ch} = 25^\circ C$ ,  $L = 3.3mH$ ,  $R_G = 25\Omega$ ,  $I_{AR} = 7.5A$

**This transistor is an electrostatic sensitive device.  
 Please handle with caution.**

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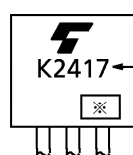
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		IGSS	VGS = ±16V, VDS = 0V	—	—	±10	μA
Drain Cut-off Current		IDSS	VDS = 250V, VGS = 0V	—	—	100	μA
Drain-Source Breakdown Voltage		V(BR)DSS	ID = 10mA, VGS = 0V	250	—	—	V
Gate Threshold Voltage		Vth	VDS = 10V, ID = 1mA	1.5	—	3.5	V
Drain-Source ON Resistance		RDS(ON)	VGS = 10V, ID = 3.5A	—	0.42	0.5	Ω
Forward Transfer Admittance		Yfs	VDS = 10V, ID = 3.5A	4	7.5	—	S
Input Capacitance		Ciss	VDS = 10V, VGS = 0V, f = 1MHz	—	700	—	pF
Reverse Transfer Capacitance		Crss		—	80	—	
Output Capacitance		Coss		—	270	—	
Switching Time	Rise Time	tr		—	10	—	ns
	Turn-on Time	ton		—	20	—	
	Fall Time	tf		—	10	—	
	Turn-off Time	toff		VIN : tr, tf < 5ns, Duty ≤ 1%, tw = 10μs	—	70	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Qg	VDD ≐ 200V, VGS = 10V	—	20	—	nC
Gate-Source Charge		Qgs	ID = 7.5A	—	13	—	
Gate-Drain ("Miller") Charge		Qgd		—	7	—	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	IDR	—	—	—	7.5	A
Pulse Drain Reverse Current	IDRP	—	—	—	30	A
Diode Forward Voltage	VDSF	IDR = 7.5A, VGS = 0V	—	—	-2.0	V
Reverse Recovery Time	trr	IDR = 7.5A, VGS = 0V	—	180	—	ns
Reverse Recovery Charge	Qrr	dIDR / dt = 100A / μs	—	1.1	—	μC

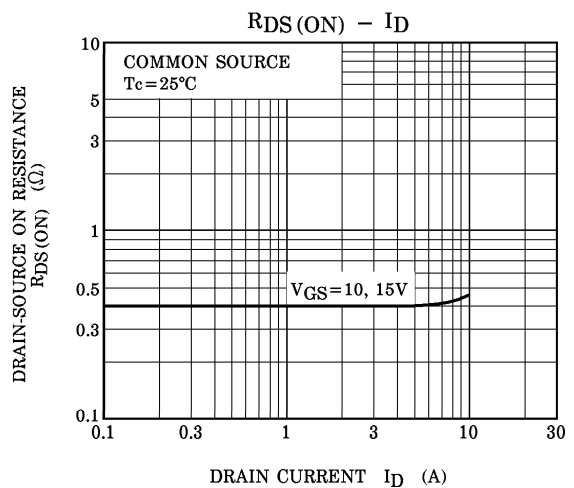
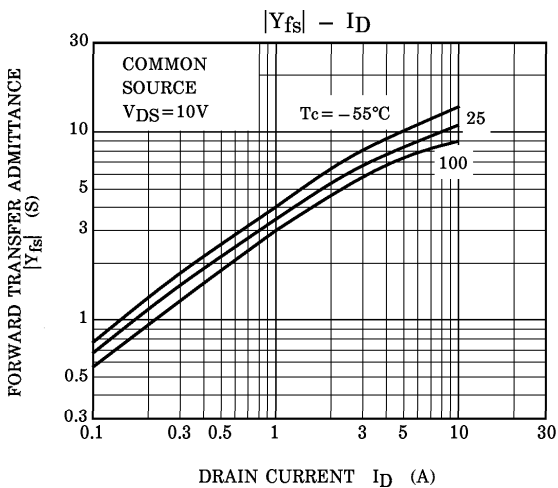
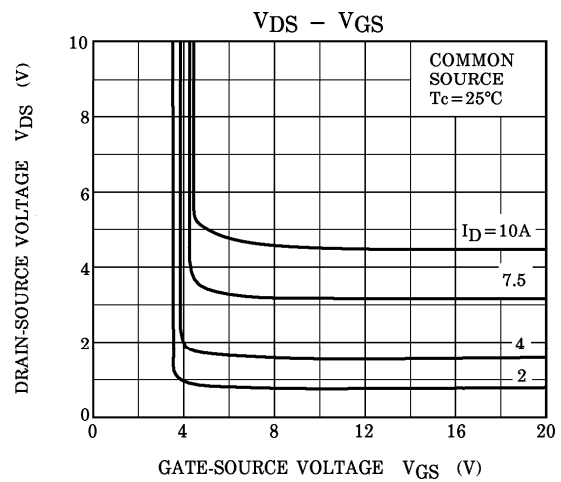
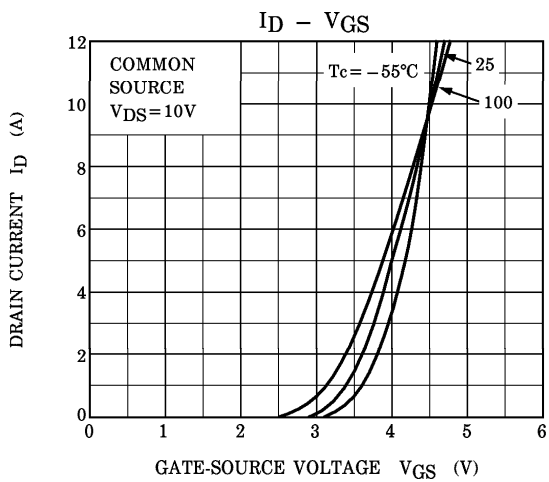
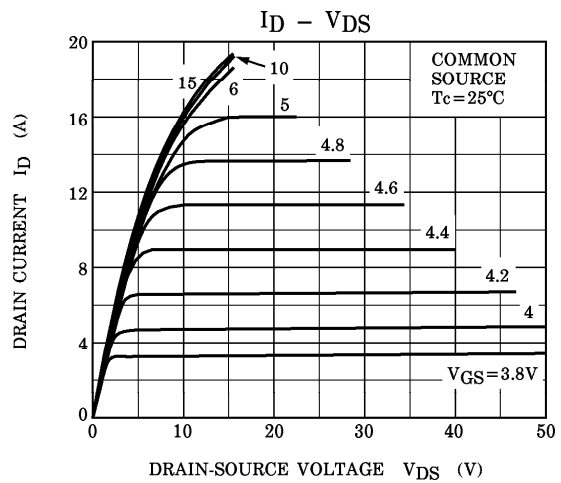
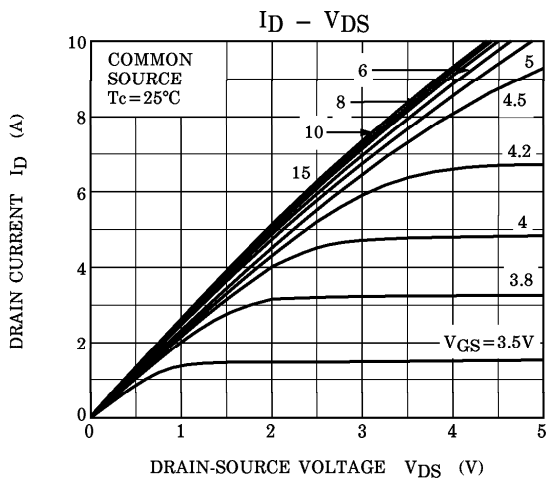
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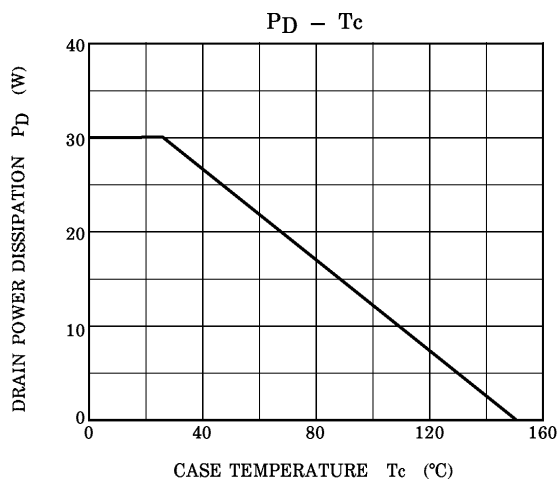
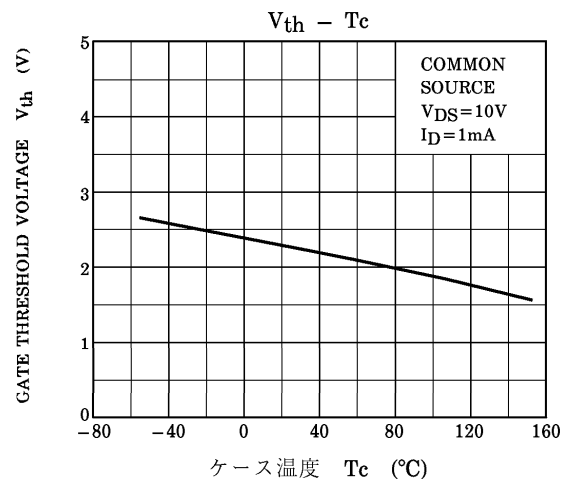
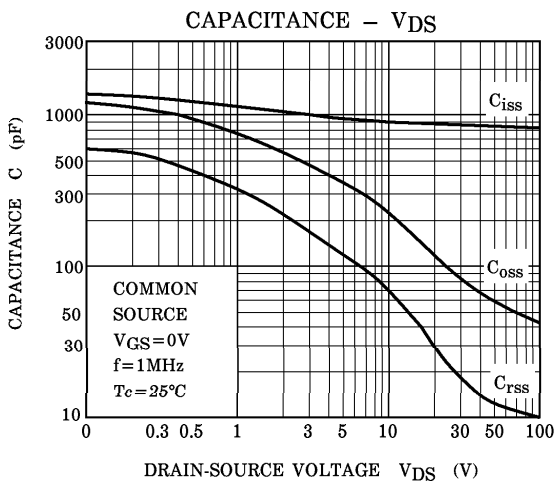
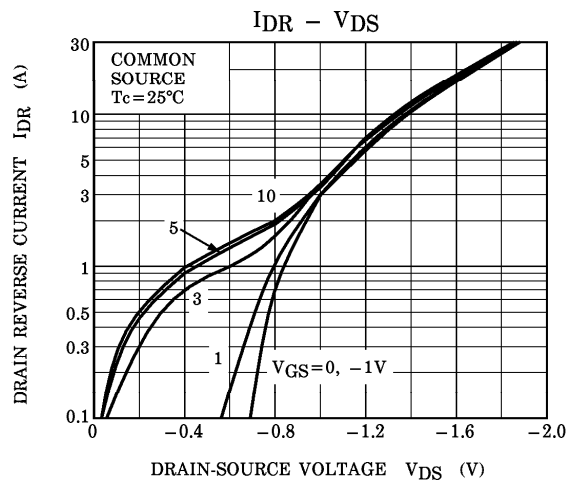
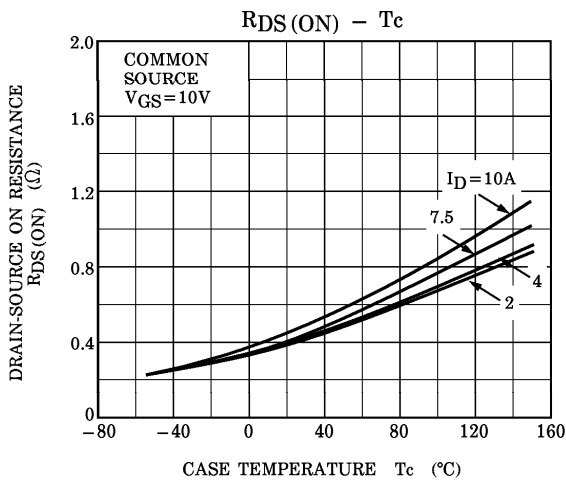


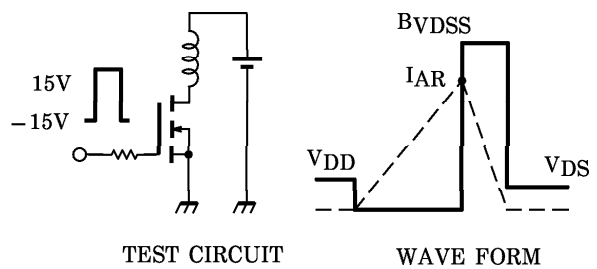
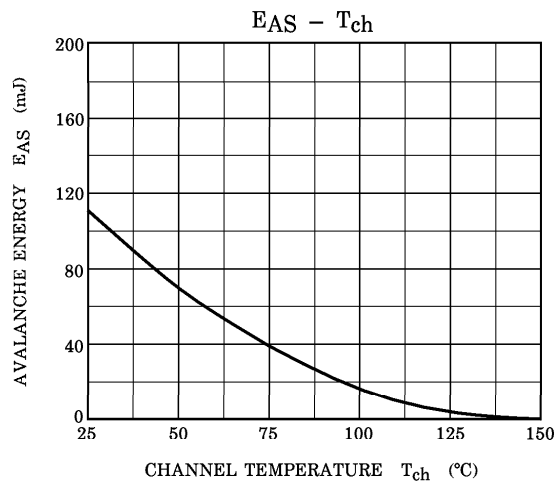
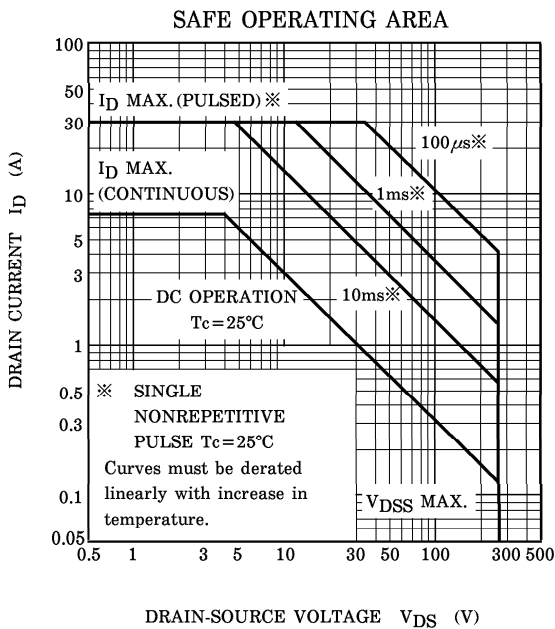
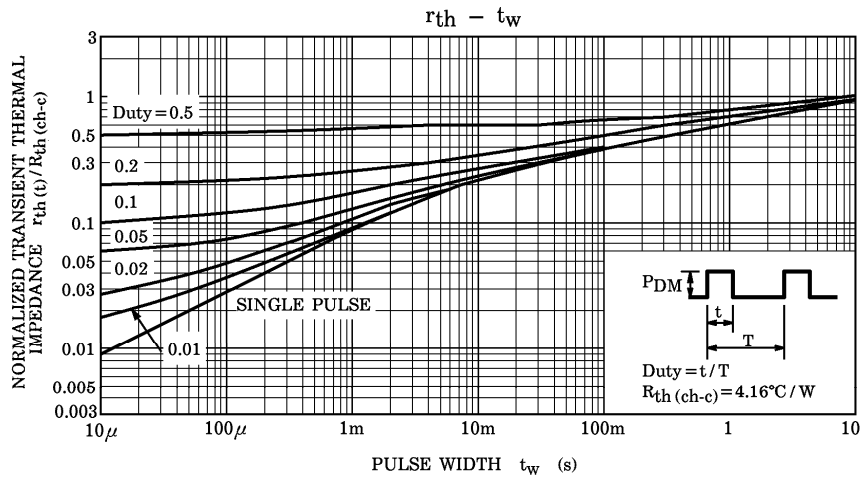
※ Lot Number

□ □ — Month (Starting from Alphabet A)

— Year (Last Number of the Christian Era)







Peak  $I_{AR} = 7.5A$ ,  $R_G = 25\Omega$

$V_{DD} = 50V$ ,  $L = 3.3mH$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$