



## P-Channel 14-V (D-S) MOSFET

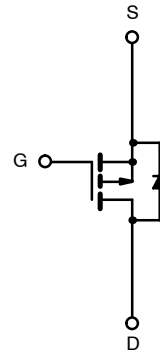
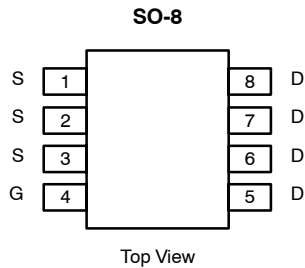
PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
-14	0.011 @ $V_{GS} = -4.5$ V	-13
	0.018 @ $V_{GS} = -2.5$ V	-10

### FEATURES

- TrenchFET® Power MOSFET
- 100%  $R_g$  Tested

### APPLICATION

- Battery Switch for Portable Devices



Ordering Information: Si4473BDY—E3 (Lead Free)  
Si4473BDY-T1—E3 (Lead Free with Tape and Reel)

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage	$V_{DS}$	-14		V	
Gate-Source Voltage	$V_{GS}$	$\pm 12$			
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$I_D$	$T_A = 25^\circ\text{C}$	-13	-9.8	A
		$T_A = 70^\circ\text{C}$	-9.9	-7.8	
Pulsed Drain Current	$I_{DM}$	-50			
continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	-2.3	-1.34		
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25^\circ\text{C}$	2.5	1.5	W
		$T_A = 70^\circ\text{C}$	1.6	0.94	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 10$ sec	40	50	$^\circ\text{C}/\text{W}$
		Steady State	70	85	
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	15	18		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

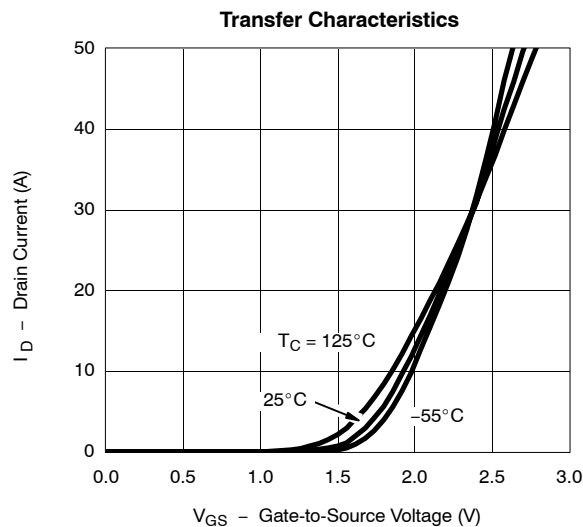
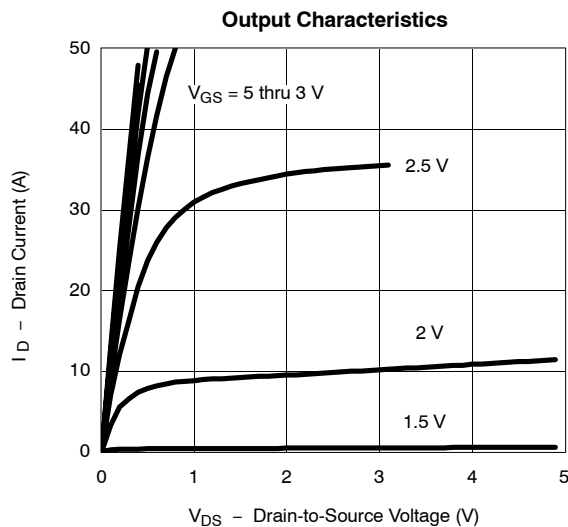
**SPECIFICATIONS (T<sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA	-0.6		-1.4	V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±12 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -14 V, V <sub>GS</sub> = 0 V			-1	μA
		V <sub>DS</sub> = -14 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C			-10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = -5 V, V <sub>GS</sub> = -4.5 V	-30			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -13 A		0.009	0.011	Ω
		V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -2.5 A		0.014	0.018	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -17 V, I <sub>D</sub> = -13 A		45		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = -2.3 A, V <sub>GS</sub> = 0 V		-0.7	-1.1	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -13 A		53	80	nC
Gate-Source Charge	Q <sub>gs</sub>			11		
Gate-Drain Charge	Q <sub>gd</sub>			22		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	1.5	2.7	4.5	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -15 V, R <sub>L</sub> = 10 Ω I <sub>D</sub> ≅ -1 A, V <sub>GEN</sub> = -4.5 V, R <sub>G</sub> = 6 Ω		55	85	ns
Rise Time	t <sub>r</sub>			95	145	
Turn-Off Delay Time	t <sub>d(off)</sub>			140	210	
Fall Time	t <sub>f</sub>			140	210	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>		I <sub>F</sub> = -2.3 A, di/dt = 100 A/μs		81	

Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

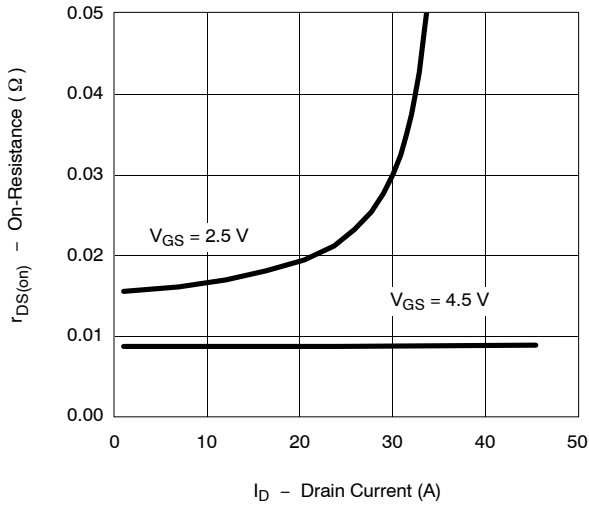
**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**



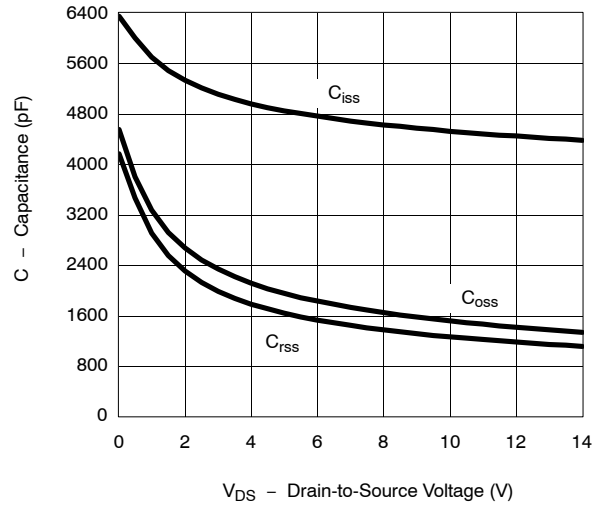


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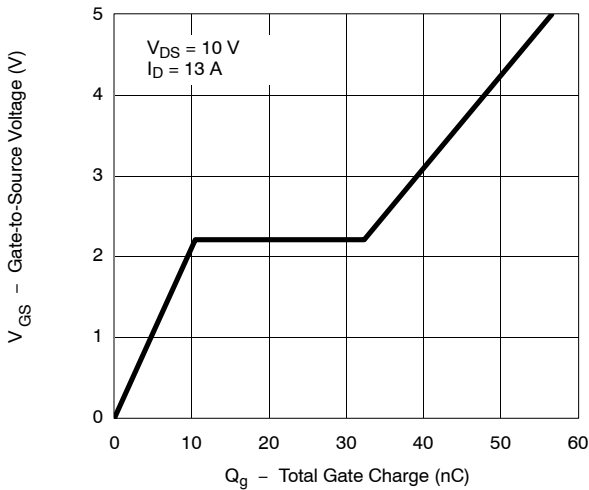
On-Resistance vs. Drain Current



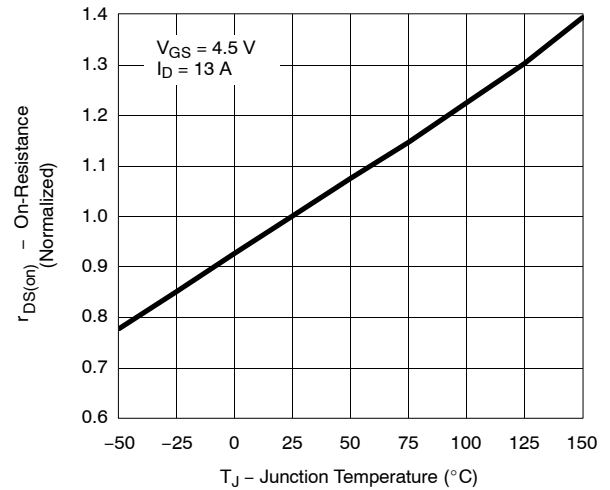
Capacitance



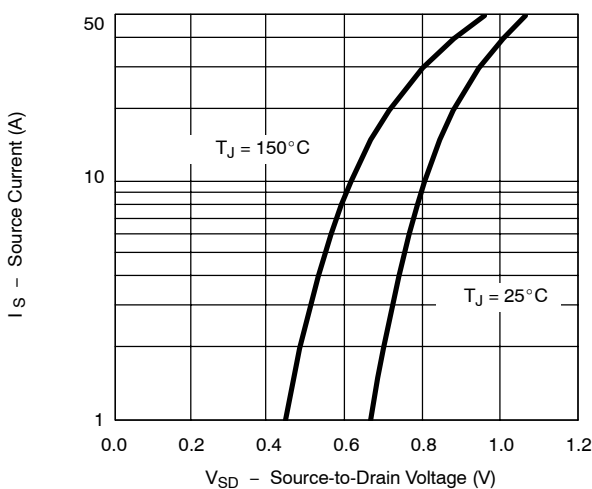
Gate Charge



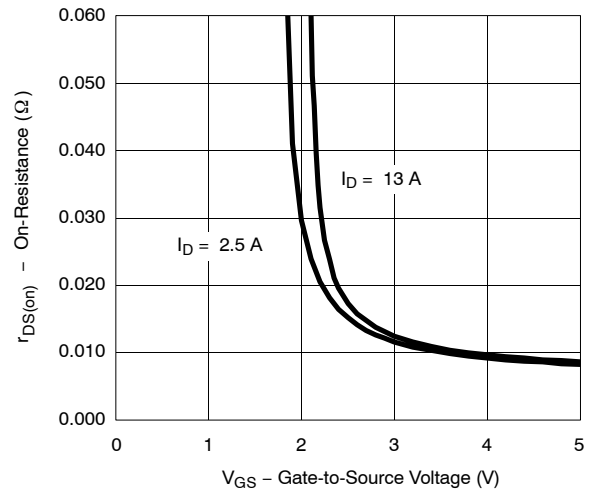
On-Resistance vs. Junction Temperature



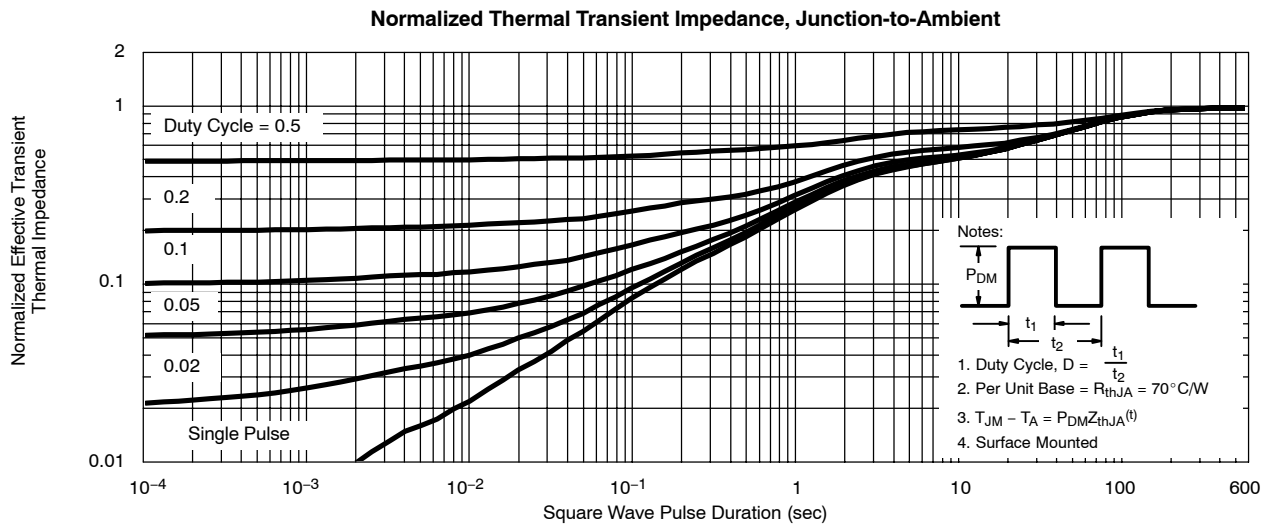
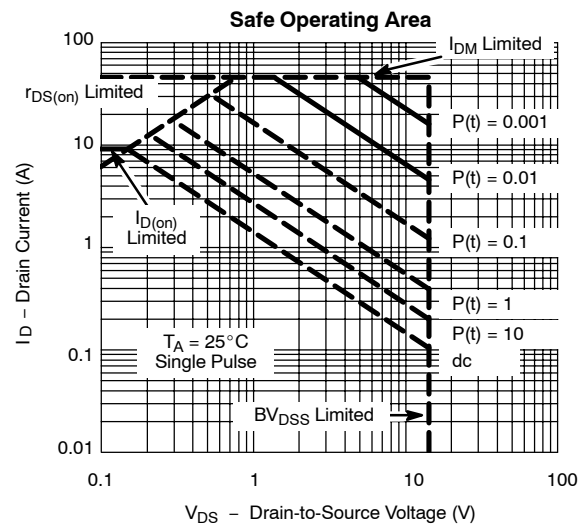
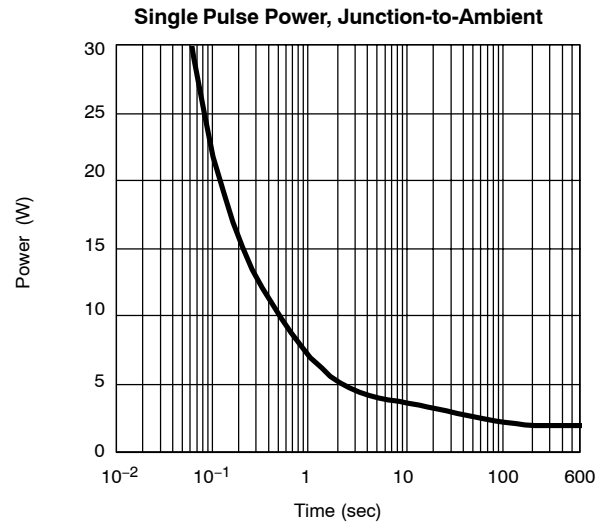
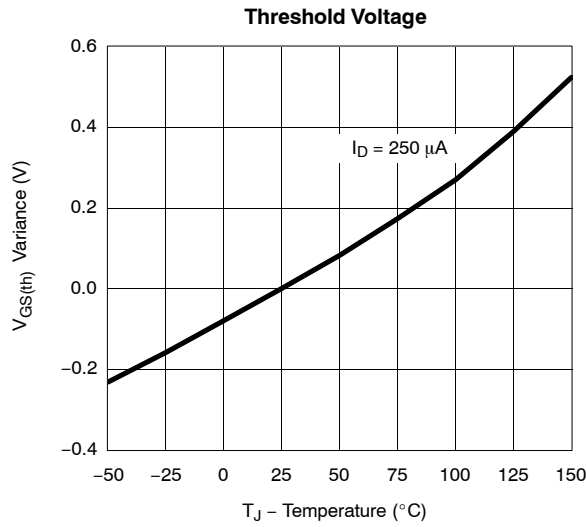
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



### TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)





**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**

Normalized Thermal Transient Impedance, Junction-to-Foot

