

### Features

- Operating voltage: 2V~12V
- Low power consumption
- Built-in oscillator needs only 5% resistor
- 0/2/4/8 data selectable
- $2^{24}$  maximum address and data codes
- Easy interface with an RF or IR medium

### Applications

- Burglar alarm system
- Smoke and fire alarm system
- Garage door controllers
- Car door controllers

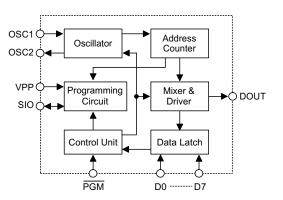
**General Description** 

# One time programmable process Data active: D0~D7

- Minimal external components
- HT6P20/A/B: 8-pin DIP/NSOP package HT6P20/D: 16-pin DIP/NSOP package
- Security system
- Cordless telephones
- Other remote control systems
- The HT6P20 is a CMOS LSI encoder designed for remote control system applications. It encodes 24 bits of information and then serially transmits it via the DOUT pin upon receipt of transmission enable (DATA pins: D0~D7) signals. The combination of address and data bits of the HT6P20 is designed using one time pro-

## **Block Diagram**

grammable process. In addition, the chip offers various packaging for flexible combination of programmable address/data so as to meet various applications. Its programmable address/data is transmitted together with the anti-code bits via RF or infrared transmission medium upon receipt of a trigger signal.



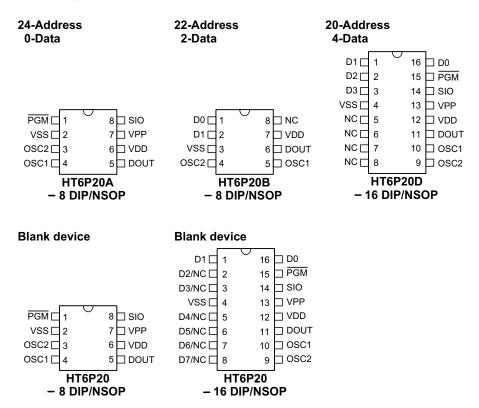
Note: Address/Data numbers are available in various combinations, refer to the functional description.

May 2, 2000

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#### **Pin Assignment**



Note: The customer code and control code have been programmed into HT6P20A/B/D by Holtek's factory process.

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The blank device HT6P20 is not programmed, it can be programmed by Holtek's programming kit.

Warning: The 8-pin blank device only allows programming as HT6P20A type.

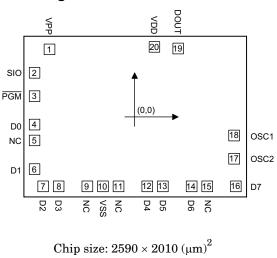
The 16-pin blank device allows programming as one of HT6P20/D type. After programming the HT6P20 pin name are the same as one of HT6P20D, which is determined by the kit programming procedure.

Pin name "NC" stands for no connection (floating).



Unit:  $\mu m$ 

# **Pad Assignment**



\* The IC substrate should be connected to VSS in the PCB layout artwork.

Pad No.	X	Y
1	-956.50	762.75
2	-1123.00	498.75
3	-1123.00	236.25
4	-1123.00	-86.25
5	-1123.00	-261.25
6	-1123.00	-583.75
7	-1027.50	-776.75
8	-852.50	-776.75
9	-530.00	-776.75
10	-355.00	-776.75
11	-180.00	-776.75
12	142.50	-776.75
13	317.50	-776.75
14	640.00	-776.75
15	815.00	-776.75
16	1137.50	-776.75
17	1118.00	-466.75
18	1118.00	-205.75
19	489.00	772.05
20	223.50	789.75

**Pad Coordinates** 

# **Pin Description**

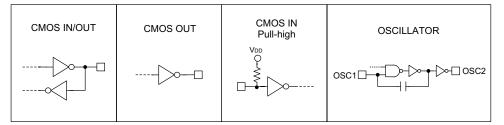
# HT6P20D

Pin No.	Pin Name	I/O	Internal Connection	Description
16 1~3	D0~D3	Ι	CMOS IN Pull-high	Data input and transmission enable (active low) They can be externally set to VSS or left open.
4	VSS			Negative power supply, ground
5~8	NC			No connection
9	OSC2	0	OSCILLATOR	Oscillator output pin
10	OSC1	Ι	OSCILLATOR	Oscillator input pin
11	DOUT	0	CMOS OUT	Data serial transmission output
12	VDD		—	Positive power supply
13	VPP	Ι		Programming power supply, $V_{\mbox{DD}}$ for normal operation
14	SIO	I/O	CMOS IN/OUT	Programming address/control code input and mode code output for mode verification
15	$\overline{\mathrm{PGM}}$	Ι	CMOS IN Pull-high	Program mode control pin, active low

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#### Approximate internal connection circuits



# **Absolute Maximum Ratings**

Supply Voltage0.3V to 12V	Storage Temperature– $50^\circ C$ to $125^\circ C$
Input VoltageV_{SS} – 0.3V to V_{DD} + 0.3V	Operating Temperature–20°C to 75°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

## **Electrical Characteristics**

Same hall Damage atom			Test Conditions		74.	<b>—</b>	М	<b>TT</b> • 4
Symbol	Parameter		V <sub>DD</sub>	Conditions	Min.	Тур.	Max.	Unit
V <sub>DD</sub>	Operating Voltage		_		2	_	12	V
I <sub>STB</sub>	Standby Current		12V	Oscillator stops		1	2	μA
I <sub>DD</sub>	Operating Current		12V	No load f <sub>OSC</sub> =3kHz		200	400	μΑ
V <sub>IH</sub>	"H" Input Voltage		_	—	0.8V <sub>DD</sub>	_	V <sub>DD</sub>	V
$V_{IL}$	"L" Input Voltage		_		0	_	$0.2 V_{DD}$	V
R <sub>PH</sub>	D0~D7 Pull-high Resistance		12V	—		150	300	kΩ
		Source	5V	$0.9 V_{DD}$	-2	-5	_	mA
I <sub>DOUT</sub> Out	O to t O		12V	$0.9 V_{DD}$	-6.5	-15		mA
	Output Current	Sink	5V	$0.1 V_{DD}$	2	5	_	mA
			12V	$0.1 V_{DD}$	6	15		mA
f <sub>OSC</sub>	Oscillator Frequency		12V	$R_{OSC}$ =1.4M $\Omega$		3		kHz

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Ta=25°C

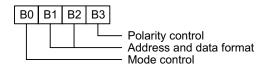


## **Functional Description**

#### Programming

The HT6P20 is a one time programmable encoding integrated circuit. The programmable codes consist of 4-bit control codes and 24-bit address codes. Before using the device, first program the internal 4 control bits and 24 address bits. Then, to program the address and control codes, connect the PGM pin to VSS, allowing the SIO pin to receive control and address codes from the continuously transmitting programming kit. The control codes (B0~B3) and address timing are respectively shown on the Table and the Figure for Programming Timing. In the programming process, the mode bit (B0) has to be programmed first. The 0/2/4/8 data number is selected by the address along with the data format bits (B1,B2). The HT6P20, in addition, provides a polarity bit (B3) to select the polarity of the output data.

Various packages of the HT6P20 offer programmable combinations of address/data to meet various application needs.



#### Table 1: Control code

• Mode control (B0)

<b>B0</b>	Function
0	Not programmed
1	Programmed

• Address and data format (B1,B2)

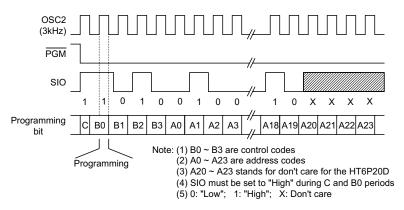
<b>B2</b>	<b>B1</b>	Code Format
0	0	A0~A23
0	1	A0~A21 + D1~D0
1	0	A0~A19 + D3~D0
1	1	A0~A15 + D7~D0

• Polarity control (B3)

<b>B</b> 3	Data Output Polarity
0	Negative polarity
1	Positive polarity

The HT6P20A/D can be programmed by the SIO pins. The HT6P20B, on the other hand, is programmed for 2-data and positive polarity application.

Programming timing of the HT6P20D (B0~B3=1010)



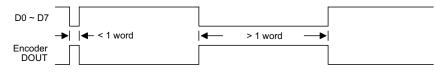
Programming timing for the HT6P20D

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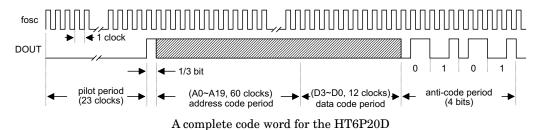
#### Normal operation

The HT6P20 encodes and transmits address/data to a decoder upon receipt of a trigger signal. The address codes of the HT6P20A are always transmitted as long as power (VDD) is supplied. The transmission function of the HT6P20B/D is enabled by the D0~D7 pins (active low). The following is the transmission timing of the HT6P20:

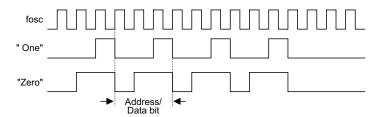


Transmission timing

A complete code word of the HT6P20D consists of 3 periods as shown below.



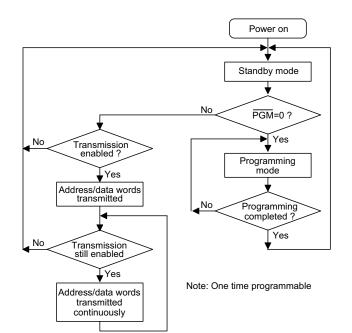
The HT6P20A/B/D detects the logic state of the internal programmed address and the external data pins, and then transmits the detected information during the code period. Each address/data bit can be set to one of the following two logic states:



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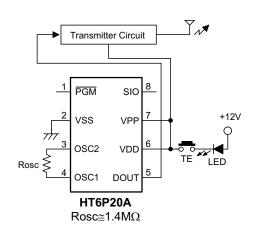


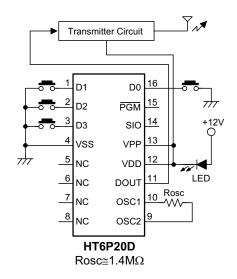
#### Flowchart



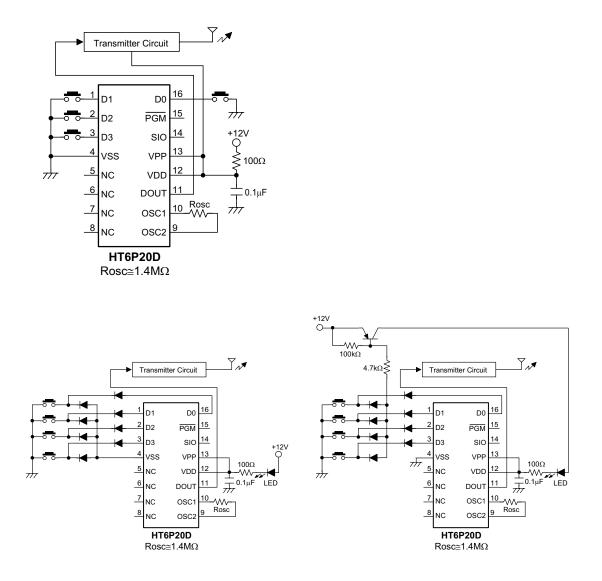
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# **Application Circuits**









Note: In order to prevent the IC from getting damaged due to the latch up, the 100 $\Omega$  resistor or the LED which can also be a transmission indicator is indispensible when V\_{DD}=9V~12V.

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