DATA SHEET



N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR HIGH SPEED SWITCHING

DESCRIPTION

The μ PA611TA is a switching device which can be driven directly by a 2.5-V power source.

The μ PA611TA has excellent switching characteristics, and is suitable for use as a high-speed switching device in digital circuits.

FEATURES

- Can be driven by a 2.5-V power source
- Low gate cut-off voltage

ORDERING INFORMATION

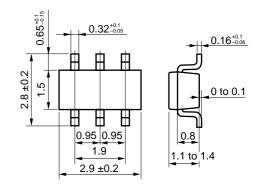
PART NUMBER	PACKAGE
μΡΑ611ΤΑ	SC-74 (Mini Mold)

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage	VDSS	30	V
Gate to Source Voltage	Vgss	±20	V
Drain Current (DC)	D(DC)	±0.1	А
Drain Current (pulse) ^{Note}	D(pulse)	±0.4	А
Total Power Dissipation	P⊤	300 (TOTAL)	mW
Channel Temperature	T_{ch}	150	°C
Storage Temperature	Tstg	-55 to +150	°C

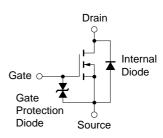
Note PW \leq 10 μ s, Duty Cycle \leq 1 %

PACKAGE DRAWING (Unit : mm)

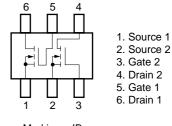


EQUIVALENT CIRCUIT

(1/2 Circuit)



PIN CONNECTION (Top View)



Marking : IB

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

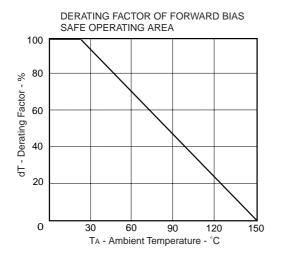
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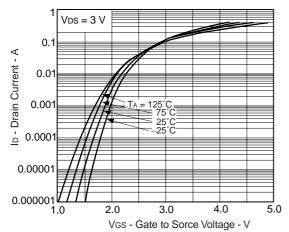
ELECTRICAL CHARACTERISTICS (TA = 25 °C)

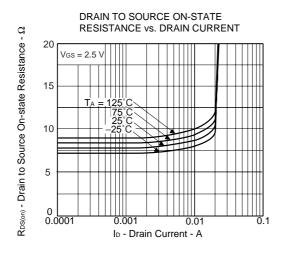
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	IDSS	$V_{DS} = 30 V$, $V_{GS} = 0 V$			1	μA
Gate Leakage Current	lgss	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±10	μA
Gate Cut-off Voltage	V _{GS(off)}	$V_{DS} = 3 V$, $I_D = 10 \mu A$	1.0	1.4	1.8	V
Forward Transfer Admittance	y _{fs}	V _{DS} = 3 V, I _D = 10 m A	20			mS
Drain to Source On-state Resistance	RDS(on)1	Vgs = 2.5 V, Id = 1 m A		8	15	Ω
	RDS(on)2	V _{GS} = 4 V, I _D = 10 mA		4	8	Ω
	RDS(on)3	Vgs = 10 V, Id = 10 mA		3	5	Ω
Input Capacitance	Ciss	V _{DS} = 3 V		9		pF
Output Capacitance	Coss	V _{GS} = 0 V		12		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		2.1		pF
Turn-on Delay Time	td(on)	V _{DD} = 3 V		40		ns
Rise Time	tr	ID = 10 mA		55		ns
Turn-off Delay Time	td(off)	V _{GS(on)} = 4 V		68		ns
Fall Time	tr	R _G = 10 Ω, R _L = 300 Ω		64		ns

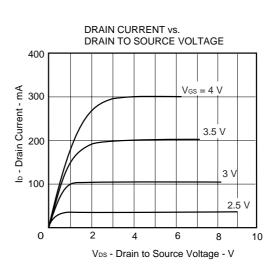
TYPICAL CHARACTERISTICS (TA = 25 °C)



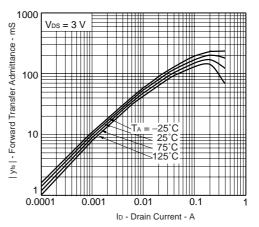




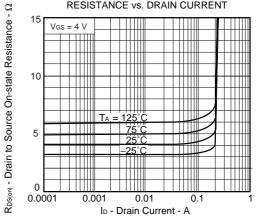




FORWARD TRANSFER ADMMITTANCE Vs. DRAIN CURRENT



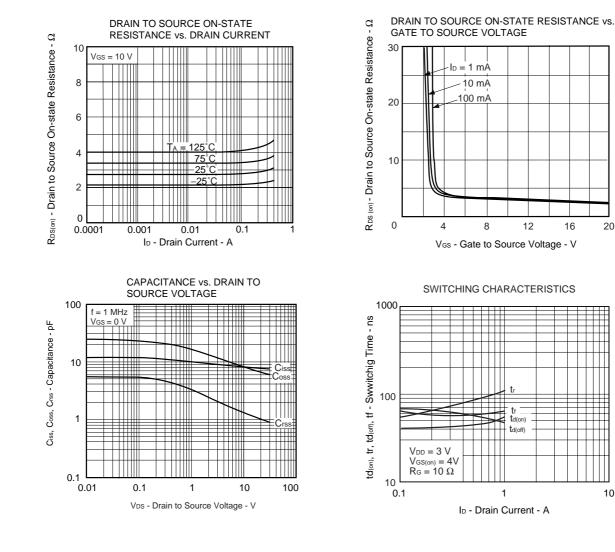
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



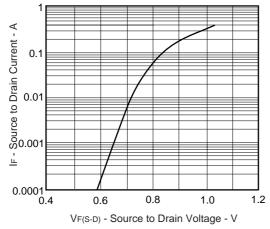
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SOURCE TO DRAIN DIODE FORWARD VOLTAGE



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REFERENCE

Document Name	Document No.		
NEC semiconductor device reliability / quality control system	TEI-1202		
Quality grade on NEC semiconductor devices	C11531E		
Semiconductor device mounting technology manual	C10535E		
Guide to quality assurance for semiconductor devices	MEI-1202		
Semiconductor selection guide	X10679E		

[MEMO]

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[MEMO]

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