

# **Dual Diode Modules**

Replaces December 1998 version, DS5106-2.0

DS5106-3.0 January 2000

**KEY PARAMETERS** 

**CIRCUIT OPTIONS** 

2100V

440A

2500V

11250A

## **FEATURES**

- Dual Device Module
- Electrically Isolated Package
- Pressure Contact Construction
- International Standard Footprint
- Alumina (non-toxic) Isolation Medium

#### **APPLICATIONS**

- Rectifier Bridges
- DC Power Supplies
- Plating Rectifiers
- Traction Systems

**VOLTAGE RATINGS** 

# G •

FSM

 $\mathbf{V}_{\mathsf{isol}}$ 

Code

HB

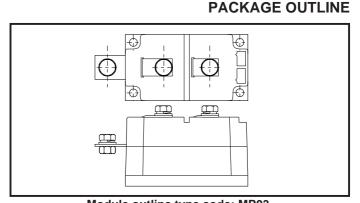
GN

I<sub>F(AV)</sub>(per arm)

Circuit

#### Repetitive Conditions **Type** Number **Peak Voltages** $V_{RRM}$ MP03/440 - 21 2100 $T_{vi} = 150^{\circ}C$ $I_{RM} = 30 \text{mA}$ MP03/440 - 20 2000 MP03/440 - 18 $V_{RSM} = V_{RRM} + 100V$ 1800 MP03/440 - 16 1600 respectively

Lower voltage grades available. For full description of part number see "Ordering Instructions" on page 3.



Module outline type code: MP03. See Package Details for further information.

#### **CURRENT RATINGS - PER ARM**

Symbol	Parameter	Conditions		Max.	Units
I <sub>F(AV)</sub>		Halfwave, resistive load	$T_{case} = 75^{\circ}C$	440	Α
			$T_{case} = 85^{\circ}C$	390	Α
	Mean forward current		T <sub>heatsink</sub> = 75°C	340	А
			T <sub>heatsink</sub> = 85°C	300	А
I <sub>F(RMS)</sub>	RMS value	T <sub>case</sub> = 75°C	,	690	А

# **SURGE RATINGS - PER ARM**

Symbol	Parameter	Conditions		Max.	Units
I <sub>FSM</sub>	Surge (non-repetitive) on-state current	T _ 1500C	V <sub>R</sub> = 0	11250	А
			V <sub>R</sub> = 50% V <sub>RRM</sub>	9000	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	10ms half sine;	$V_R = 0$	630000	A <sup>2</sup> s
	Trioriusing	T <sub>j</sub> = 150°C	V <sub>R</sub> = 50% V <sub>RRM</sub>	405000	A <sup>2</sup> s

# **THERMAL & MECHANICAL RATINGS**

Symbol	Parameter	Conditions	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - junction to case per Diode	dc	0.12	°C/W
		halfwave	0.13	°C/W
		3 phase	0.14	°C/W
R <sub>th(c-hs)</sub>	Thermal resistance - case to heatsink per Diode	Mounting torque = 5Nm with mounting compound	0.05	°C/W
T <sub>vj</sub>	Virtual junction temperature		150	°C
T <sub>sto</sub>	Storage temperature range		-40 to 150	°C
V <sub>isol</sub>	Isolation voltage	Commoned terminals to base plate AC RMS, 1min, 50Hz	2.5	kV

# **CHARACTERISTICS**

Symbol	Parameter	Conditions	Max.	Units
V <sub>FM</sub>	Forward voltage	At 1000A , T <sub>case</sub> = 25°C	1.29	V
I <sub>RM</sub>	Peak reverse current	At $V_{RRM}$ , $T_j = 150^{\circ}C$	30	mA
V <sub>TO</sub>	Threshold voltage	At T <sub>vj</sub> = 150°C	0.94	V
r <sub>T</sub>	On-state slope resistance	At T <sub>vj</sub> = 150°C	0.32	mΩ

#### **ORDERING INSTRUCTIONS**

Part number is made up as follows:

#### MP03 HB 440 - 18

MP = Pressure contact module

03 = Outline type

HB = Circuit configuration code (see "circuit options" - front page)

440 = Nominal average current rating at  $T_{case} = 75^{\circ}C$ 

18 =  $V_{RRM}/100$ 

#### Examples:

MP03HB440 - 21 MP03G440 - 16 MP03GN440 - 18

Note: Prefered type is HB configuration. G and GN types are available for specific applications, only when requested.

#### MOUNTING RECOMMENDATIONS

- Adequate heatsinking is required to maintain the base temperature at 75°C if full rated current is to be achieved. Power dissipation may be calculated by use of V<sub>TO</sub> and r<sub>T</sub> information in accordance with standard formulae. We can provide assistance with calculations or choice of heatsink if required.
- The heatsink surface must be smooth and flat; a surface finish of N6 (32μin) and a flatness within 0.05mm (0.002") are recommended.
- Immediately prior to mounting, the heatsink surface should be lightly scrubbed with fine emery, Scotch Brite or a mild chemical etchant and then cleaned with a solvent to remove oxide build up and foreign material. care should be taken to ensure no foreign particles remain.

- ■An even coating of thermal compound (eg. Unial) should be applied to both the heatsink and module mounting surfaces. This should ideally be 0.05mm (0.002") per surface to ensure optimum thermal performance.
- ■After application of thermal compound, place the module squarely over the mounting holes, (or 'T' slots) in the heatsink. Using a torque wrench, slowly tighten the recommended fixing bolts at each end, rotating each in turn no more than 1/4 of a revolution at a time. Continue until the required torque of 5Nm (44lb.ins) is reached at both ends.
- It is not acceptable to fully tighten one fixing bolt before starting to tighten the others. Such action may DAMAGE the module.

### **Curves**

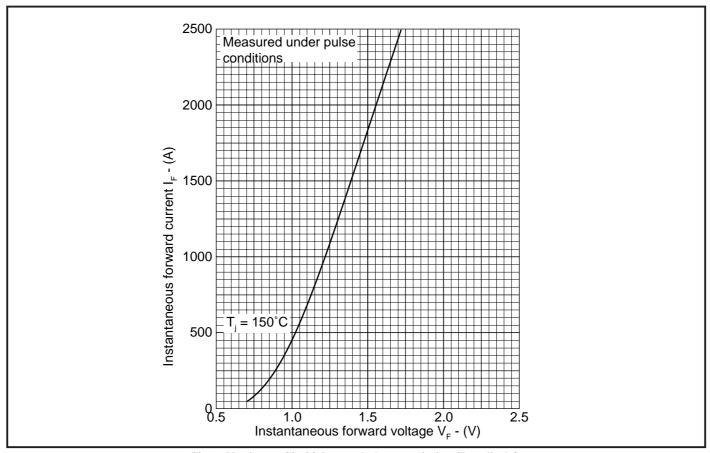


Fig. 1 Maximum (limit) forward characteristics (Per diode)

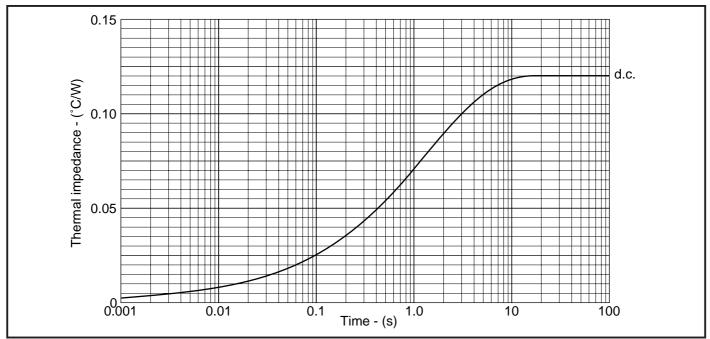


Fig. 2 Transient thermal impedance (DC) - (Per diode)

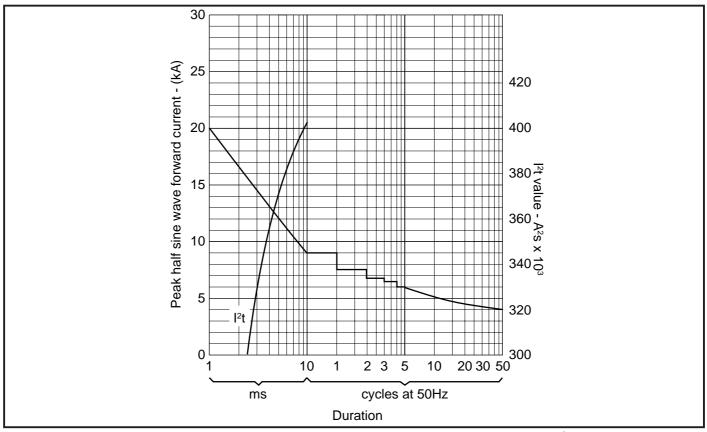


Fig. 3 Surge (non-repetitive) forward current vs time (with 0% V<sub>RRM</sub>, T<sub>case</sub> = 150°C)

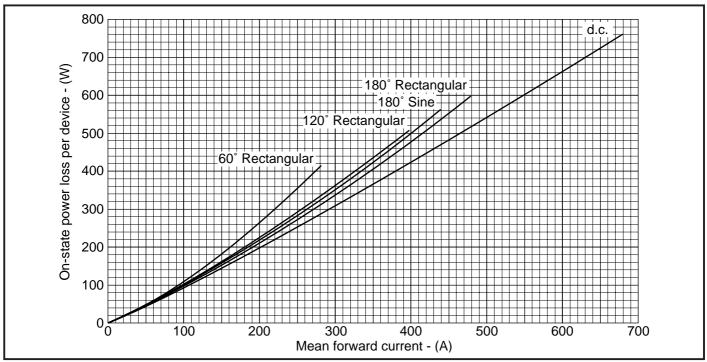


Fig. 4 On-state power loss per arm vs forward current at various conduction angles, 50/60Hz

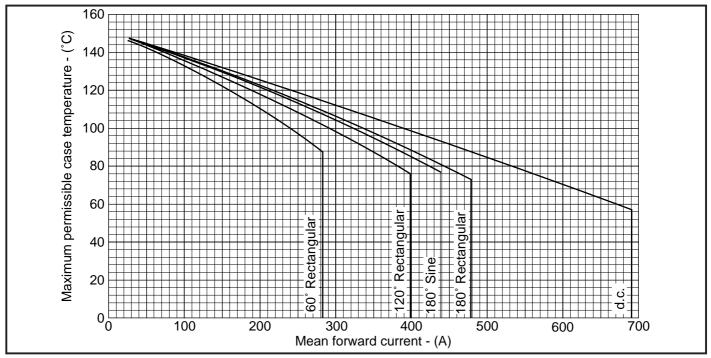


Fig. 5 Maximum permissible case temperature vs forward current per arm at various conduction angles, 50/60Hz

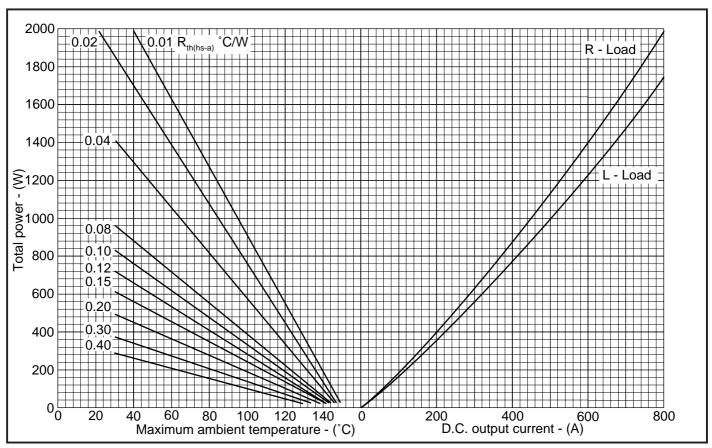


Fig. 6 50/60Hz single phase bridge dc output current vs power loss and maximum permissible ambient temperature for various values of heatsink thermal resistance.

 $(Note: R_{th(hs-a)} \ values \ given \ above \ are \ true \ heatsink \ thermal \ resistances \ to \ ambient \ and \ already \ account \ for \ R_{th(c-hs)} \ module \ contact \ thermal).$ 

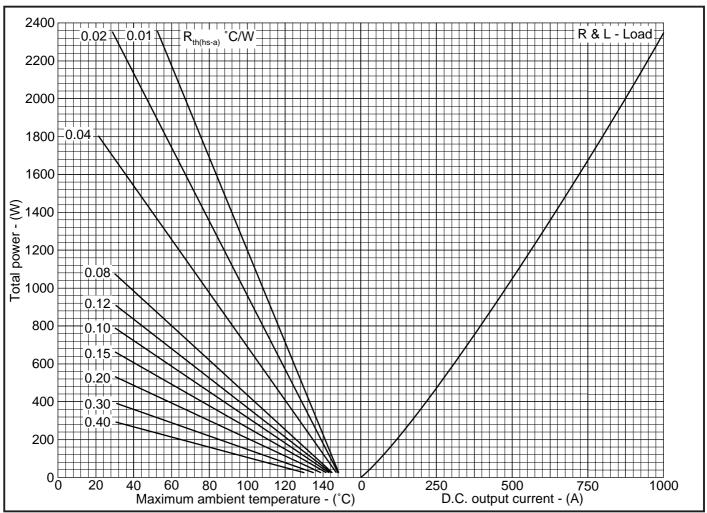
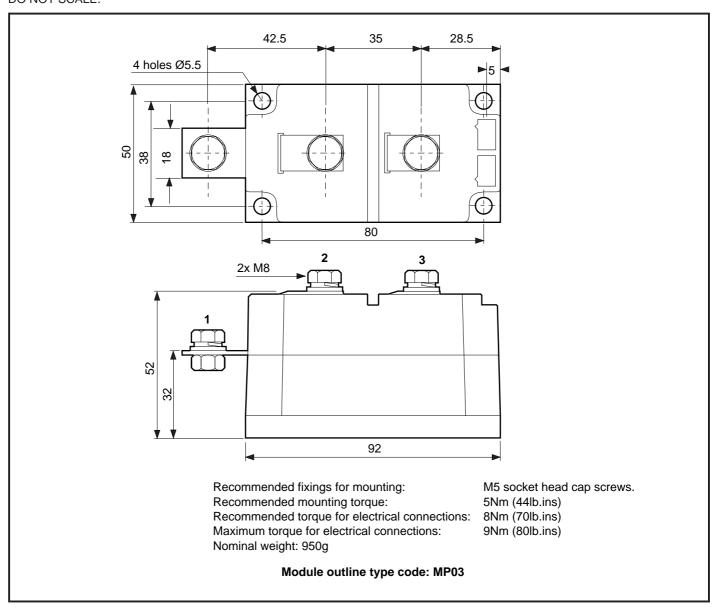


Fig. 7 50/60Hz 3- phase bridge dc output current vs power loss and maximum permissible ambient temperature for various values of heatsink thermal resistance.

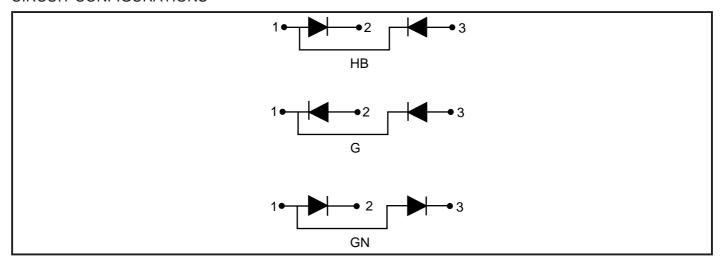
 $(Note: R_{th(hs-a)} \ values \ given \ above \ are \ true \ heatsink \ thermal \ resistances \ to \ ambient \ and \ already \ account \ for \ R_{th(c-hs)} \ module \ contact \ thermal).$ 

### **PACKAGE DETAILS**

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



# **CIRCUIT CONFIGURATIONS**





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